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A Novel Control Method

Three-Phase Multilevel Inverter

Highlights

Power Factor Detection

Input Voltage Fluctuation

Discovering Thoughts, Inventing Future

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A Novel Control Method for Improvement of Voltage Utilization Factor in Three-Phase Multilevel Inverter Considering the Input Voltage Fluctuation

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Abstract- In This paper wind, solar and fuel cells based stand alone cogeneration systems are presented for remote area utilities applications. This type of co generation system output voltages are not constant or stable in always. The generated output voltages are directly connected to the loads, without battery bank or energy storage devices. The PI, fuzzy control method was proposed in this paper using svpwm, such that the output voltage of converter circuit is constant even though input voltages are fluctuation conditions. A three phase multilevel inverter with static and dynamic load was examined to validate for proposed work in MATLAB environment.

Keywords: space vector PWM; PI-controller; FUZZY-controller; cogeneration systems; closed loop control system.

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S. Narasimha ^α & M. Sushama ^ο

Abstract- In This paper wind, solar and fuel cells based stand alone cogeneration systems are presented for remote area utilities applications. This type of co generation system output voltages are not constant or stable in always. The generated output voltages are directly connected to the loads, without battery bank or energy storage devices. The PI, fuzzy control method was proposed in this paper using svpwm, such that the output voltage of converter circuit is constant even though input voltages are fluctuation conditions. A three phase multilevel inverter with static and dynamic load was examined to validate for proposed work in MATLAB environment.

Keywords: space vector PWM; PI-controller; FUZZY-controller; cogeneration systems; closed loop control system.

I. INTRODUCTION

The last decades growth in the production of electric energy from renewable energy sources has led to an increased focus on power electronics. Renewable energy sources like photovoltaic, wind and wave energy are relying on power converters in order to exchange power with the grid. Anyone who wants to produce power for the grid has to make sure that their facilities are complying with national grid codes. The grid codes has strict regulations when it comes to the voltage quality, including limits for rapid voltage variations, flicker and harmonic distortion. Rapid voltage variations and flicker are matters of control of the inverter system, but harmonic distortion is created by the pulse width modulated switching of the converter. Different filters topologies can be used in order to reduce the harmonics generated by the switching action in the converter.

The technology growth in the recent years, most of the electrical and electronics equipments are playing the major role in the social growth. Accordingly more and more amount of power demand has an increased. One solution that to meet the power demands in rural and remote area are cogeneration systems, witch an includes hybrid renewable energy sources. In case of

the system power generation with natural energy and fuel cell, comparatively large amount of fluctuation is generated at the DC voltage.

The circuit wich converts the DC power in to AC power using multilevel inverter circuit was implemented considering the input dc voltage fluctuations. However the space vector pulse width modulation method was applied in order to improve the voltage utilization factor and stabilization of output voltage.

In this paper, the new control method which an introduced the control of the output voltage feedback control is proposed. It applied to the control circuit, multilevel inverter and operational principle with are explained.

II. DIODE CLAMPED MULTILEVEL INVERTER

The diode-clamped inverter provides multiple voltage levels through connection of the phases to a series bank of capacitors. According to the original invention, the concept can be extended to any number of levels by increasing the number of capacitors. Early descriptions of this topology were limited to three-levels where two capacitors are connected across the dc bus resulting in one additional level. The additional level was the neutral point of the dc bus, so the terminology neutral point clamped (NPC) inverter was introduced. However with an even number of voltage levels, the neutral point is not accessible, and the term multiple point clamped (MPC) is sometimes applied. Due to capacitor voltage balancing issues, the diode-clamped inverter implementation has been mostly limited to the three level. Because of industrial developments over the past several years, the three level inverter is now used extensively in industry applications. Although most applications are medium-voltage, a three-level inverter for 480V is on the market.

The three phase 3-level diode clamped multilevel inverter is the common multilevel inverter used for various applications. A three phase 3-level diode clamped multilevel inverter is adopted in this project. It is obtained from a configuration of twelve switching devices and six clamping diodes as shown in figure.

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The pairs Sa1 Sa1', Sa2 Sa2', Sb1 Sb1', Sb2 Sb2', Sc1 Sc1' and Sc2 Sc2' are complementary. Therefore, Sa1'=1-Sa1, Sa2'=1-Sa2, Sb1'=1-Sb1, Sb2'=1-Sb2, Sc1'=1-Sc1 and Sc2'=1-Sc2. There are twelve active combinations were taken using these switching states which produce twelve active voltage vectors. The nonzero voltage vectors are from V1 to V12.

The sector is identified from three phase reference voltage and the corresponding voltage vector is selected from the switching table to generate the gating pulses for the inverter. The vector sequence is V1, V2, V3, V4, V5, V6, V7, V8, V9, V10, V11, V12, V1 each for 30°.

The angle of reference is found using equation $= \tan^{-1} \left(\frac{v_{ld}}{v_{lq}} \right)$. In 3-level inverter one cycle is split into twelve sectors with each 30°. Sector 1 is from 0 to +30°, sector 2 is from +30° to +60°, sector 3 is from +60° to +90°, sector 4 is from +90° to +120°, sector 5 is from +120° to +150° and the sector 6 is from +150° to +180°, Sector 7 is from -180° to 150°, sector 8 is from -150° to -120°, sector 9 is from -120° to -90°, sector 10 is from -90° to -60°, sector 11 is from -60° to -30° and the sector 6 is from -30° to 0°.

The switching table is formed using the sector, the corresponding voltage vector and the switch state. For example, the angle of the reference voltage is between 0° and 30°, it is in sector 1 and it selects the voltage vector V1. The corresponding switching state is 110000. Switches Sa1 and Sa2 are in on state. Switches Sb1, Sb2, Sc1 and Sc2 are in off state. Switches Sa1', Sa2', Sb1', Sb2', Sc1' and Sc2' are complementary.

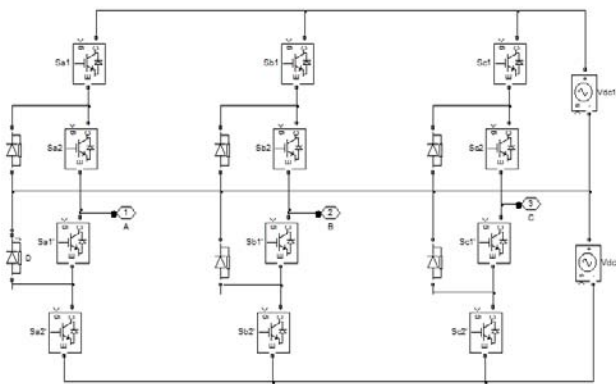


Figure 1 : 3-level diode clamped inverter

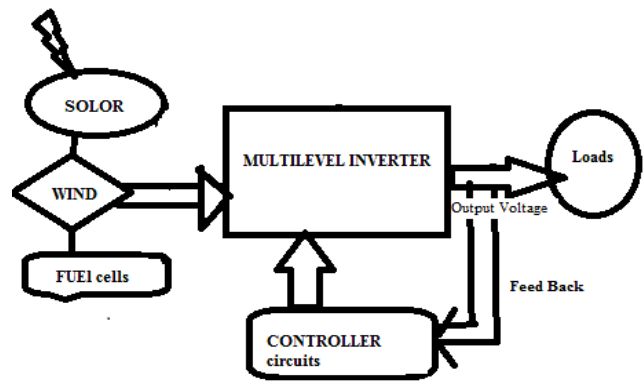


Figure 2 : Block diagram of the output voltage control system

From fig 2. It represent the combined generation systems of the solar energy, wind power and fuel cell systems are consider for the input source to the inverter circuit, these are generated power is not constant in always. In such cases output voltage suffers from its input. In this paper we developed a control circuit with PI, Fuzzy controller logic which absolves the output voltages, generates the equalant correction signal to the pwm circuits.

III. SPACE-VECTOR PWM

One of the features of two parallel connected inverters is the ability to obtain three levels of voltage (phase to neutral) $P = V_{DC}$, $H = \frac{1}{2} V_{DC}$ and $O = 0$. Describing system in the meaning of three voltage levels provides similarity to Three-Level Neutral Point Clamped Inverter. This similarity allows to approach Space Vector Modulation in the same way like for Neutral Point Clamped Inverter [1].

The large vectors divides the plane into six sectors. Each of these sectors can be split into four regions as it is depicted in Fig. 3. Combination of vectors which should be used to synthesize V_{ref} is based on its position. For example, when V_{ref} is in region four of sector I, V2, V7 and V14 are used.

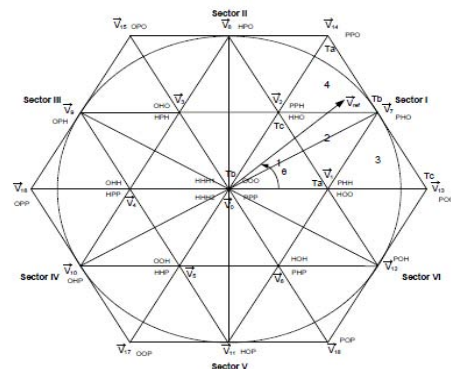


Figure 3 : 3-level space vector modulation sectors

a) Dwell Times

The following Figure presents reference vector transition from sector I to sector II:

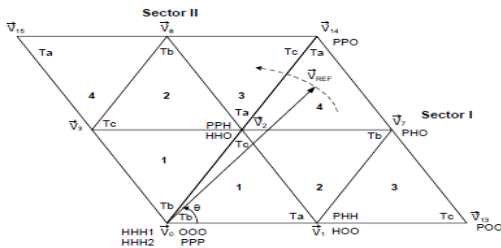


Figure 4 : Voltage reference transition between two sectors

where:

$$V_{14} = \frac{2}{3}V_{DC}e^{j\frac{\pi}{3}}; V_7 = \frac{2}{3}V_{DC}e^{j\frac{\pi}{6}}; V_2 = \frac{1}{3}V_{DC}e^{j\frac{\pi}{3}}; \frac{2}{3}[\cos(\frac{\pi}{3}) + j\sin(\frac{\pi}{3})]T_a + \frac{\sqrt{3}}{3}[\cos(\frac{\pi}{6}) + j\sin(\frac{\pi}{6})]T_b + \frac{1}{3}[\cos(\frac{\pi}{6}) + j\sin(\frac{\pi}{6})]T_c = \frac{V_{ref}}{V_{DC}}[\cos(\Theta) + j\sin(\Theta)]T_s \quad (2)$$

And splitting into real and imaginary part:

b) Real

$$\frac{1}{3}T_a + \frac{1}{2}T_b + \frac{1}{6}T_c = \frac{V_{ref}}{V_{DC}}\cos(\Theta)T_s \quad (3)$$

c) Imaginary

$$\frac{\sqrt{3}}{3}T_a + \frac{\sqrt{3}}{6}T_b + \frac{\sqrt{3}}{6}T_c = \frac{V_{ref}}{V_{DC}}\sin(\Theta)T_s \quad (4)$$

d) Together with

$$T_a + T_b + T_c = T_s \quad (5)$$

a set of equations real and imaginary can be created to calculate dwell times: T_a , T_b , T_c . The dwell times for every region in sector I are presented in Table1.

Table 1 : Switching pattern for sector

SECTOR -1				
Segments	1	2	3	4
1	[OOO]	[HOO]	[HOO]	[HHO]
2	[HOO]	[HHO]	[POO]	[PHO]
3	[HHO]	[PHO]	[PHO]	[PPO]
4	[HHH]	[PHH]	[PHH]	[PPH]
5	[HHO]	[PHO]	[PHO]	[PPO]
6	[HOO]	[HHO]	[POO]	[PHO]
7	[OOO]	[HOO]	[HOO]	[HHO]

O-lower switching, H-upper switching, P-both upper and lower switching.

IV. PI CONTROLLER

PI controller will eliminate forced oscillations and steady state error resulting in operation of on-off controller and P controller respectively. However, introducing integral mode has a negative effect on speed of the response and overall stability of the

The dwell times calculation for three level SVM is similar to two level SVM. Vectors: V_2 , V_7 and V_{14} are used when V_{ref} is in region four of sector I:

$$V_{14}T_a + V_7T_b + V_2T_c = V_{ref}T_s \quad (1)$$

system. Thus, PI controller will not increase the speed of response [5]. It can be expected since PI controller does not have means to predict what will happen with the error in near future [1] [2]. This problem can be solved by introducing derivative mode which has ability to predict what will happen with the error in near future and thus to decrease a reaction time of the controller. PI controllers are very often used in industry, especially when speed of the response is not an issue. A control without D mode is used when

- Fast response of the system is not required
- large disturbances and noise are present during operation of the process
- There is only one energy storage in process (capacitive or inductive)
- There are large transport delays in the system

The pi controller basic mathematical equation is

$$U = Kp e_k + Ki \sum_{j=0}^k e_k + u_0 \quad (6)$$

$$u_k = \begin{cases} U_{max}(U \geq U_{max}) \\ U_{min}(U \leq U_{min}) \end{cases}$$

Kp =proportional coefficient

Ki =integral coefficient

e_k =K-th sampling time

u_0 =initial value

V. FUZZY CONTROLLER

The most commonly used fuzzy inference technique is the so-called Mamdani method, as the very first attempt to control a steam engine and boiler combination by synthesizing a set of linguistic control rules obtained from experienced human operators. Their work was inspired by an equally influential Interest in fuzzy control has continued ever since, and the literature on the subject has grown rapidly [2]. In this model the fuzzy implication is modeling by Mamdani's minimum operator, the conjunction operator is min, the t-norm

from compositional rule is min and for the aggregation of the rules the max operator is used. In order to explain the working with this model of FLC will be considered the example. Where a simple two-input one-output problem that includes three rules is examined

Rule 1 : IF x is A3 OR y is B1 THEN z is C1

Rule 2 : IF x is A2 AND y is B2 THEN z is C2

Rule 3 : IF x is A1 THEN z is C3.

$$\mu_{A1}(x_0) = 0.5, \mu_{A2}(x_0) = 0.2, \mu_{B1}(y_0) = 0.1, \mu_{B2}(y_0) = 0.7 \quad (7)$$

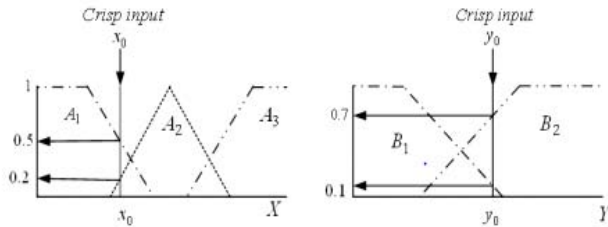


Figure 5 : Fuzzification

Step 2: Rules evaluation

The fuzzified inputs are applied to the antecedents of the fuzzy rules. If a given fuzzy rule has multiple antecedents, the fuzzy operator (AND or OR) is used to obtain a single number that represents the result of the antecedent evaluation. To evaluate the disjunction of the rule antecedents, one uses the OR fuzzy operation. Typically, the classical fuzzy operation unions used.

$$\mu_{A \cup B}(x) = \max\{\mu_A(x), \mu_B(x)\} \quad (8)$$

Similarly, in order to evaluate the conjunction of the rule antecedents, the AND fuzzy operation intersection is applied.

$$\mu_{A \cap B}(x) = \min\{\mu_A(x), \mu_B(x)\} \quad (9)$$

The result is given in this Figures

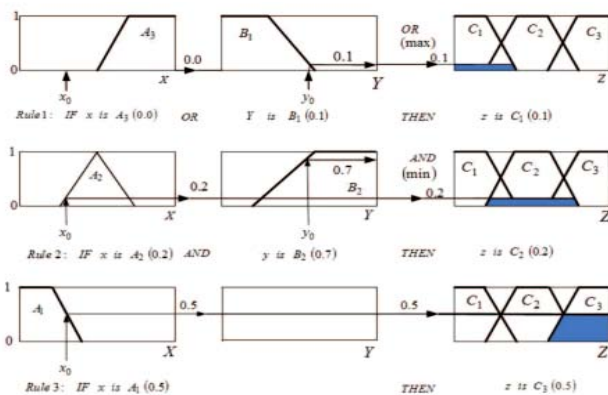


Figure 6 : Rules evaluation

Now the result of the antecedent evaluation can be applied to the membership function of the consequent. The most common method is to cut the

Step 1: Fuzzification

The first step is to take the crisp inputs, x_0 and y_0 , and determine the degree to which these inputs belong to each of the appropriate fuzzy sets. According to Fig 5 one obtains.

consequent membership function at the level of the antecedent truth; this method is called clipping. Because top of the membership function is sliced, the clipped fuzzy set loses some information. However, clipping is preferred because it involves less complex and generates an aggregated output surface that is easier to defuzzify. Another method, named scaling, offers a better approach for preserving the original shape of the fuzzy set [3][9]. The original membership function of the rule consequent is adjusted by multiplying all its membership degrees by the truth value of the rule antecedent see in fig 7.

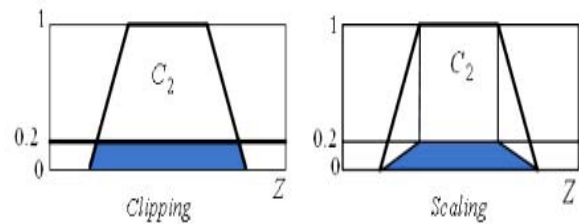


Figure 7 : Clipping and scaling

Step 3: Aggregation of the rule outputs

The membership functions of all rule consequents previously clipped or scaled are combined into a single fuzzy set. fig 8.

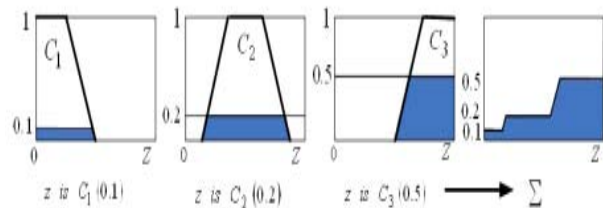


Figure 8 : Aggregation of the rule outputs

Step 4: Defuzzification

The most popular defuzzification method is the centroid technique used in this paper. It finds a point representing the center of gravity (COG) of the aggregated fuzzy set A, on the interval $[a, b]$.

A reasonable estimate can be obtained by calculating it over a sample of points. This fuzzy logic controller is 3- level system so it has two inputs, n-level system has $(n-1)$ inputs and $(n-1)^2$ rules are used so in this fuzzy logic controllers.



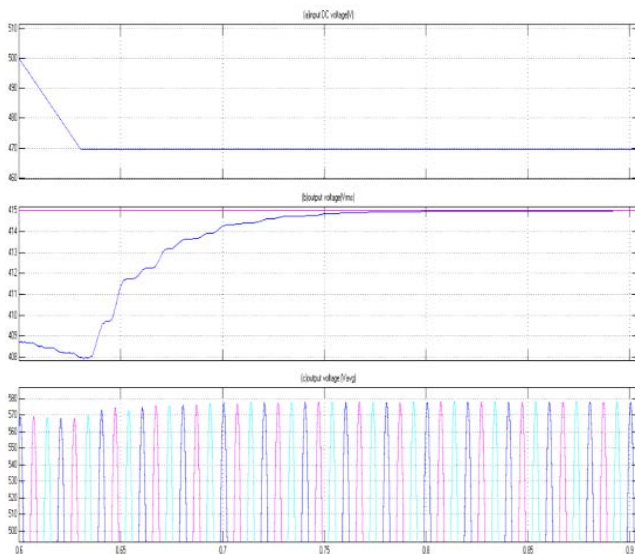


Figure 13 : Simulation results of Multilevel inverter using PI controller with zoom scale of fig12. (a) input DC voltage,(b) Inverter output voltage (Vms) (c) the output voltage (Vavg)

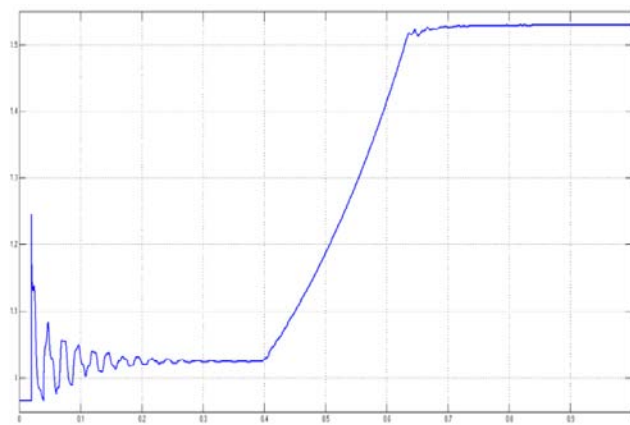


Figure 14 : PI controller error signal

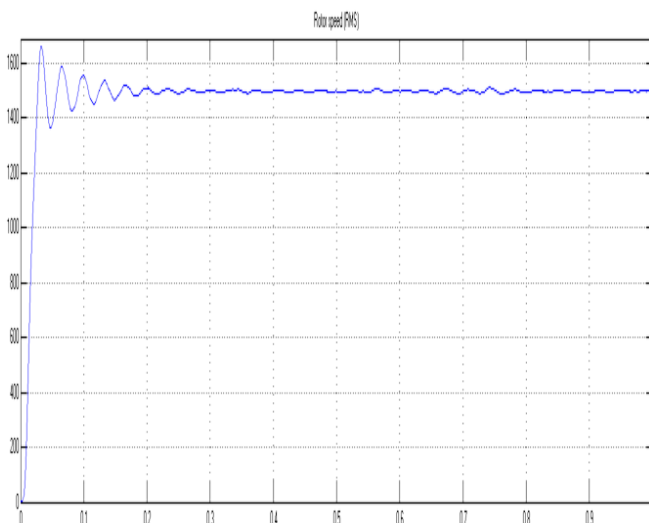


Figure 15 : Rotor speed of induction motor (rpm)

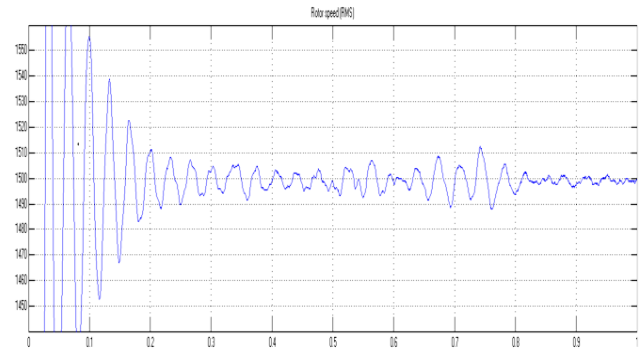


Figure 16 : Rotor speed of induction motor with zoom scale(rpm)

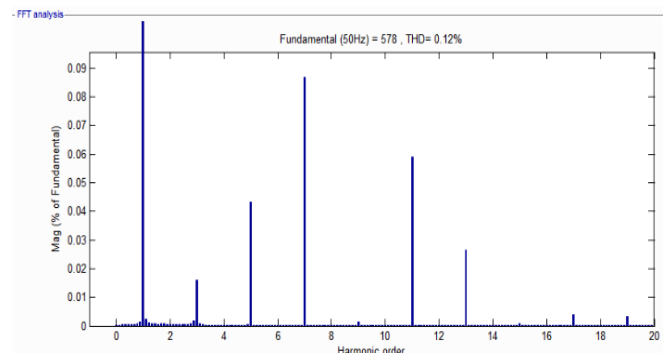


Figure 17 : THD with static load

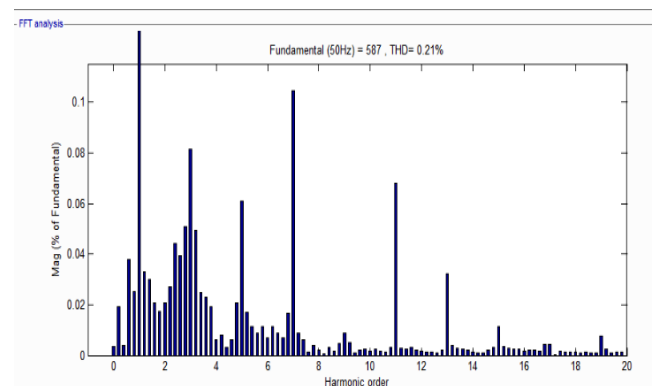


Figure 18 : THD with Induction motor load

From fig. no. 15, we conclude that using PI control method the 20% input DC voltage fluctuation was an effectively absorbed and improved the output rated voltage magnitude considering dc voltage fluctuation, induction motor load was running with constant rotor speed even though dc voltage fluctuation cases.

b) Fuzzy logic controller

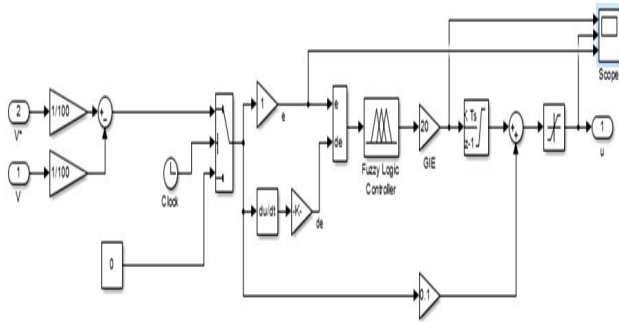


Figure 19 : Simulation diagram of Fuzzy- controller

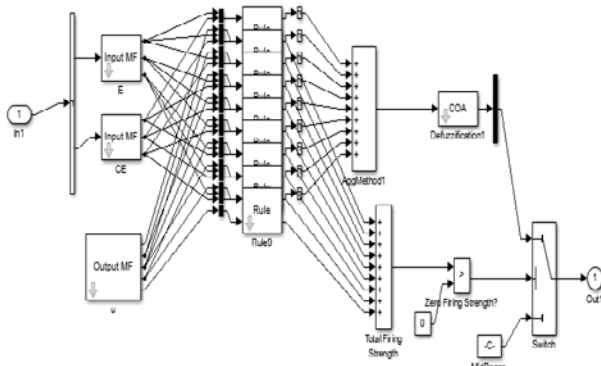


Figure 20 : Sub system diagram of Fuzzy logic simulation block

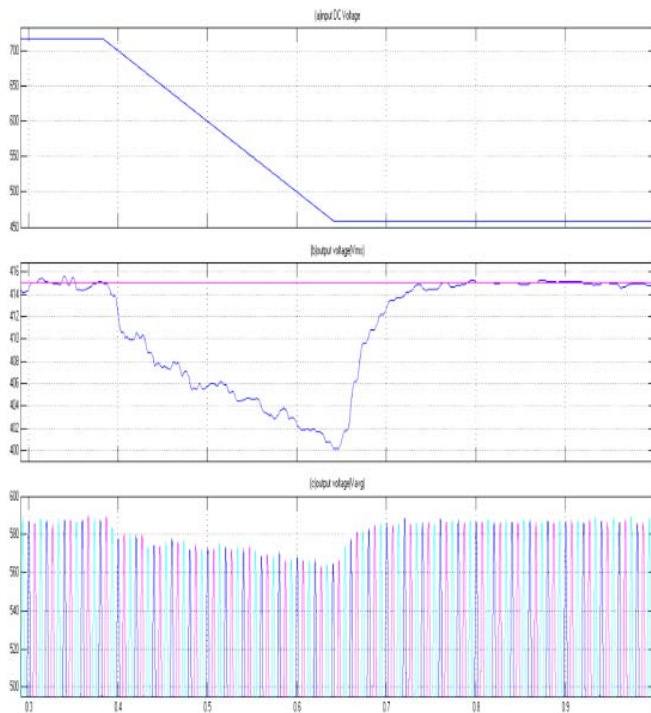


Figure 21 : Simulation results of Multilevel inverter using Fuzzy controller with 22% dc voltage fluctuation.(a) input DC voltage,(b) Inverter output voltage (Vrms) (c) the output voltage (Vavg)

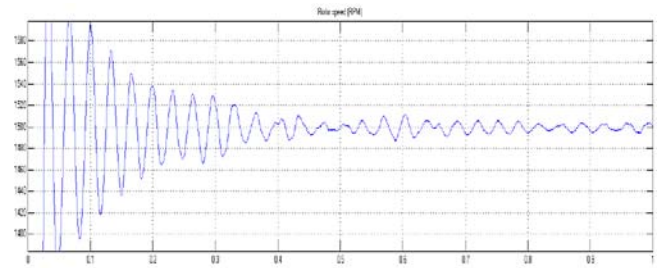


Figure 22 : Imulation results of dynamic load rotor speed using fuzzy-controller

From fig. no. 21. (b) it shows the output (Vrms) voltage from 0sec. to 0.4sec. the DC voltage magnitude is constant, voltage (Vrms) is less oscillated. Then from 0.4sec.to 0.62sec.DC voltage is gradually decreases but output voltage (Vrms) also decreased very smoothly. Then after input DC voltage is constant magnitude with 22% lesser value. From 0.62sec. to max time scale, the voltage (Vrms) is has an increased to reached the rated value magnitude of 417v within very less time comparatevly PI control method.

From fig.no.21(c) we can observe that 0sec. to 0.4sec. input DC is constant, and the output voltage is maintain constant magnitude. From 0.4sec. to 0.62sec. the input DC voltage is decreases. In this case the output voltage magnitude is small decreased. Than after time scale from 0.62sec. to 0.8sec. the output voltage has increased to reach the rated value of magnitude. after 0.62sec. to 1sec. DC voltage is 22% lesser than the applied voltage magnitude even through this period output voltage magnitude is remains constant voltage magnitude.

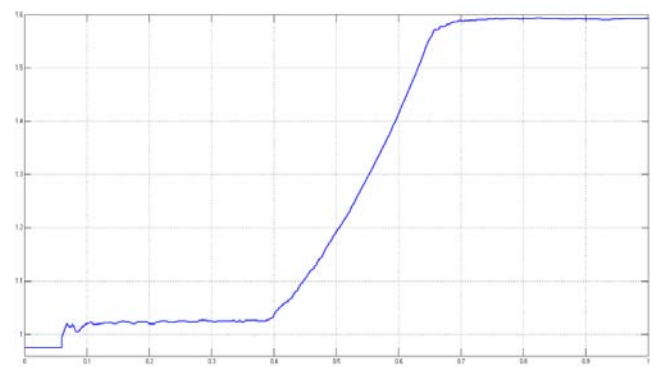


Figure 23 : Fuzzy controller error signal

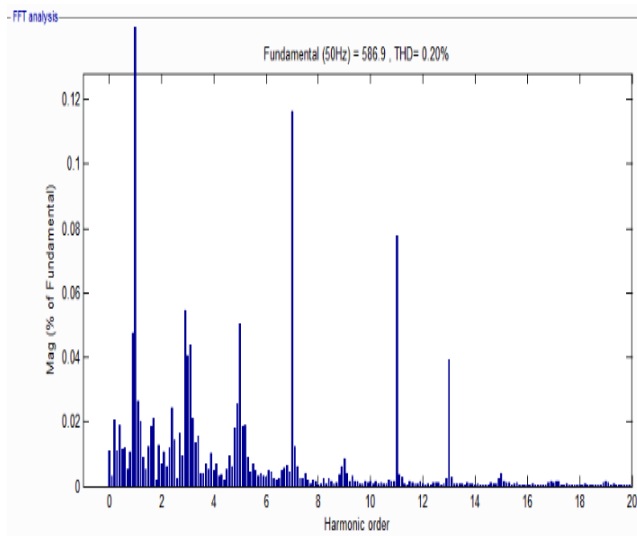


Figure 24 : THD of the dynamic load using Fuzzy controller

Three-Phase Series RLC Load (mask) (link)
Implements a three-phase series RLC load.

Parameters Load Flow

Configuration Y (grounded)

Nominal phase-to-phase voltage Vn (Vrms)
415

Nominal frequency fn (Hz):
50

Active power P (W):
6000

Inductive reactive power QL (positive var):
0

Capacitive reactive power Qc (negative var):
0

Measurements None

Figure 25 : Static load parameters(6Kw)

Block Parameters: Asynchronous Machine SI Units

Configuration Parameters Advanced Load Flow

Preset model: 15: 5.4 HP (4KW) 400 V 50Hz 1430 RPM

Mechanical input: Torque Tm

Rotor type: Squirrel-cage

Reference frame: Synchronous

Mask units: SI

Figure 26 : Dynamic load parameters

Table 2 : Results comparison between PI and Fuzzy controller

Input DC voltage fluctuation (%)	Control methods with Load	Total Harmonic Distortion (%)	No. of cycles to reach rated voltage
20	PI with Resistive	0.12	12
20	PI with Induction motor	0.21	12
22	Fuzzy with Resistive	0.20	8
22	Fuzzy with Induction motor	0.20	8

VII. CONCLUSION

From table 2. Comparison of PI and FUZZY-controllers, The advantage of PI controller is less THD than fuzzy. The advantage of Fuzzy -controller is taking the less number of cycles to reach the rated output voltage magnitude considering input dc voltage fluctuation. Fuzzy control is 22% of voltage fluctuation more DC voltage utilization and dynamic, static load response is very smooth manner than the PI control. So that fuzzy-control method is an effective one considering dc voltage fluctuation cases to improve the output voltages of converters with very fast response time, low THD value, and the supporting results are an examined in this paper with 6Kw resistive and 5HP induction motor loads.

VIII. FEATURE SCOPE

Future work will focus on the fast response time to reach the rated voltage magnitude considering input DC voltage fluctuation, low value of THD. Can be an implement the other hybrid techniques control algorithm. For improving the voltage utilization factor and power quality.

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Power Factor Detection using Android Application via Bluetooth

By Md. Faysal Chowdhury, Sayeedul Mursalin, Mohammad Jubair Hossain
& Omor Ahmed Dhali

American International University, Bangladesh

Abstract- In present days Technological advancement and its incorporation is becoming a significant role in human life. Now Electrical power is very precious but due to the addition of Inductive load the reactive power is increasing rapidly as a result the industrialization has been affecting the efficiency of the electric power system. To minimize the reactive power consumption the power factor detection system is became a serious issue. The developed module will be an ideal possibility in the upcoming future with minimal cost and flexibility. In this project we have used atmega16 microcontroller, LCD, current sensor, voltage sensor, Bluetooth Module and Android Application. The microcontroller is used to measure the phase voltage and current by using ADC as well as it detect the power factor by measuring the phase difference between voltage and current using delay. The current sensor is used to measure the current respectively voltage sensor for voltage. The LCD is used to show the measured data and the Bluetooth module is to send the data. The android application is used to show the data in smart phone which is specially developed for it. This is a part of smart grid. The ultimate objective of the project is to monitor the consumer end status continuously with minimum cost.

Keywords: microcontroller, transformer, LCD, bluetooth module, bridge rectifier, zero crossing detector, android application.

GJRE-F Classification : FOR Code: 290901



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Power Factor Detection using Android Application via Bluetooth

Md. Faysal Chowdhury ^α, Sayeedul Mursalin ^σ, Mohammad Jubair Hossain ^ρ & Omor Ahmed Dhali ^ω

Abstract- In present days Technological advancement and its incorporation is becoming a significant role in human life. Now Electrical power is very precious but due to the addition of Inductive load the reactive power is increasing rapidly as a result the industrialization has been affecting the efficiency of the electric power system. To minimize the reactive power consumption the power factor detection system is became a serious issue. The developed module will be an ideal possibility in the upcoming future with minimal cost and flexibility. In this project we have used atmega16 microcontroller, LCD, current sensor, voltage sensor, Bluetooth Module and Android Application. The microcontroller is used to measure the phase voltage and current by using ADC as well as it detect the power factor by measuring the phase difference between voltage and current using delay. The current sensor is used to measure the current respectively voltage sensor for voltage. The LCD is used to show the measured data and the Bluetooth module is to send the data. The android application is used to show the data in smart phone which is specially developed for it. This is a part of smart grid. The ultimate objective of the project is to monitor the consumer end status continuously with minimum cost.

Keywords: microcontroller, transformer, LCD, bluetooth module, bridge rectifier, zero crossing detector, android application.

I. INTRODUCTION

Power factor is the ratio between the kW and the kVA drawn by an electrical load where the kW is the actual load power and the kVA is the apparent load power. Simply, it is a measure of how efficiently the load current is being converted into useful work output and more particularly is a good indicator of the effect of the load current on the efficiency of the supply system. The value for the power factor can theoretically vary between 0/% and 100%, where a value of 100% also called unity power factor – delivers all of the power as active power. A value of 0% would mean all the power is supplied as reactive power; no motors would turn and no useful work could be accomplished. A high power factor is important. But if the power factor is low the Current will be increased, and this high current will cause to the following disadvantages.

- Large Line Losses (Copper Losses) will occurs.
- Large kVA rating and Size of Electrical Equipment's will be required.
- Greater Conductor Size and Cost will be needed
- Poor Voltage Regulation and Large Voltage Drop.
- Low Efficiency.
- Penalty from Electric Power Supply Company on Low Power factor.

This Project focuses on the design and implementation of power factor Detection using Atmega16 microcontroller chip, determine the power factor of the loaded power system, and generate proper action to calculate Capacitor. Also we would be using concepts of Bluetooth Module and Android Application.

II. PROPOSED SYSTEM

Microcontroller base automatic Detection of power factor with load monitoring is shown in fig.1

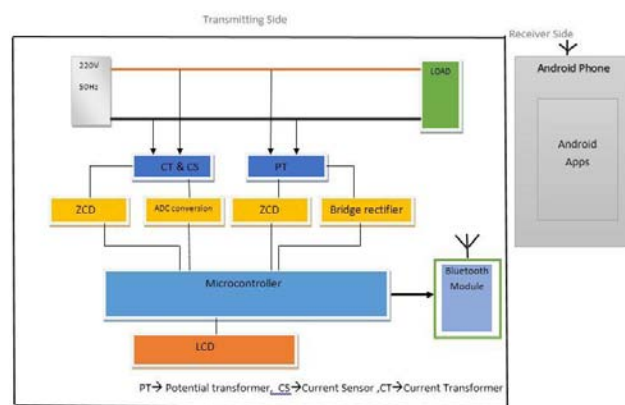


Figure 1 : Block diagram arrangement of the project

The principal element in the circuit is Atmega16 microcontroller. The current and voltage are measured from the main AC line (L) by using Hall Effect current sensor and Potential Transformer. The potential transformer and current transformer are used to measure the phase difference between voltage and current. The signals from potential transformer and current transformer are pass in to the zero crossing detector IC (ZCD I & ZCD V) individually that transposed square-wave of current and voltage and connect it to the Microcontroller to observe the zero crossing of current and voltage at the same time instant. Bridge Rectifier is used to convert the AC voltage to DC voltage. Voltage divider applied to convert the dc voltage to 5v. Hall

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Effect current sensor gives the digital signal of current. Microcontroller read the value for both of voltage and current. The microcontroller internal timer read the time difference between voltage and current and convert it to phase angle and measures the power factor. The load monitoring status are shown in LCD and send it via Bluetooth module through UART for android application.

III. CIRCUIT DESIGN ARRANGEMENT

In figure 1, the basic arrangement of the implemented project can be found.

Among the major components required to establish the project, few of them are the power transformers (step down), microcontroller ATMEGA 16 and Bluetooth module, Bridge rectifier, Zero crossing detector.

a) Transformer

Transformer is an electrical device which transfer energy from one circuit to another circuit without change its frequency but in different voltage level. In this project we have 230v to 12v step down transformer. Step down transformers convert electrical voltage from one level usually down to a lower level. A step down transformer has less turns on the secondary coil than the primary coil. The induced voltage across the secondary coil is less than the applied voltage across the primary coil or in other words the voltage is "stepped-down". Step down transformers are made from two or more coils of insulated wire wound around a core made of iron. When voltage is applied to one coil (frequently called the primary or input) it magnetizes the iron core, which induces a voltage in the other coil, (frequently called the secondary or output). The turn's ratio of the two sets of windings determines the amount of voltage transformation.



Figure 2 : 220v/12v transformer

b) Microcontroller ATMEGA 16

ATmega16 is an 8-bit high performance microcontroller of Atmel's Mega AVR family with low power consumption. Atmega16 is based on enhanced RISC (Reduced Instruction Set Computing, Know more

about RISC and CISC Architecture) architecture with 131 powerful instructions. Most of the instructions execute in one machine cycle. Atmega16 can work on a maximum frequency of 16MHz. ATmega16 has 16 KB programmable flash memory, static RAM of 1 KB and EEPROM of 512 Bytes. The endurance cycle of flash memory and EEPROM is 10,000 and 100,000, respectively. ATmega16 is a 40 pin microcontroller. There are 32 I/O (input/output) lines which are divided into four 8-bit ports designated as PORTA, PORTB, PORTC and PORTD. ATmega16 has various in-built peripherals like USART, ADC, Analog Comparator, SPI, JTAG etc. Each I/O pin has an alternative task related to in-built peripherals [3].

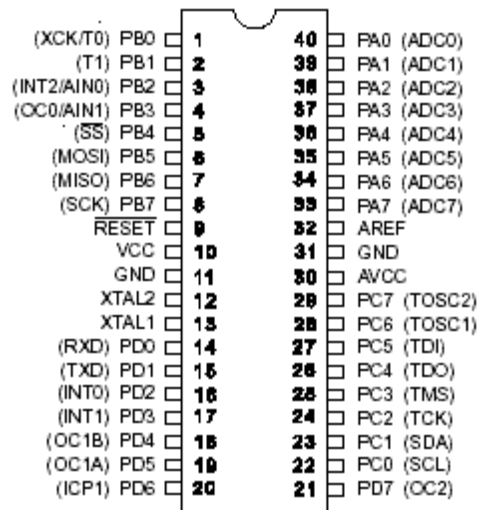


Figure 3 : Atmega16 microcontroller

c) Bluetooth module

HC-06 has been used as Bluetooth module. The Baud rate is 9600. Master and slave mode can't be switched in this Module. HC-06 module have paired memory to remember last slave device. The working voltage is 3.3V, but it can work at 3.00-4.2v. The Current pairing 20~30mA, connected 8mA [4][8].

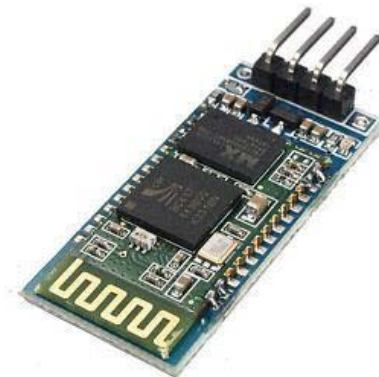


Figure 4 : Bluetooth module HC-06

d) Bridge rectifier

A bridge rectifier is an arrangement of four or more diodes in a bridge circuit configuration which provides the same output polarity for either input polarity. It is used for converting an alternating current (AC) input into a direct current (DC) output. A simple rectifier circuit described in this project converts the input from AC source to DC voltage. Firstly, the step down transformer converts the AC mains supply of 230V to 12V AC. This 12V AC is applied to the bridge rectifier arrangement such that the alternate diodes conduct for each half cycle producing a pulsating DC voltage consisting of AC ripples. A capacitor connected across the output allows the AC signal to pass through it and blocks the DC signal, thus acting as a high pass filter. The output across the capacitor is a smooth DC signal [8].

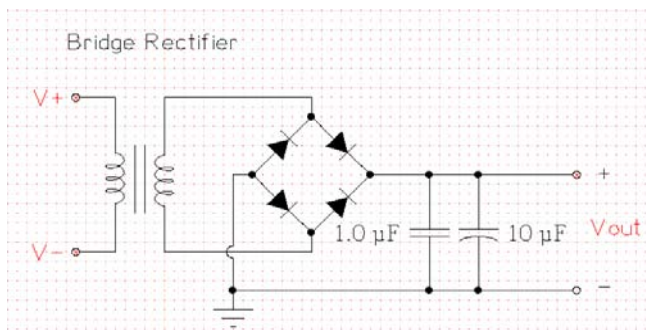


Figure 5 : Bridge rectifier

e) Zero crossing Detector

The zero crossing detector is a device that is used to detect the point where the voltage and current crosses zero in either direction. The reference voltage in this case is set to zero. The output voltage waveform shows when and in what direction an input signal crosses zero volt. If input voltage is a low frequency signal, then output voltage will be less quick to switch from one saturation point to another. And if there is noise in between the two input nodes, the output may fluctuate between positive and negative saturation voltage V_{sat} . Here IC LM358n is used as a zero crossing detector.

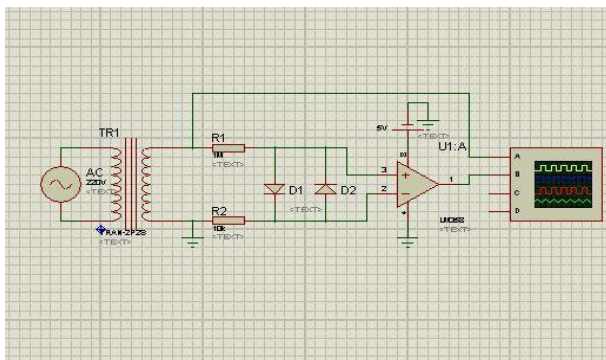


Figure 6 : zero crossing detector

The output of the zero crossing detector circuit is shown in Fig 7.

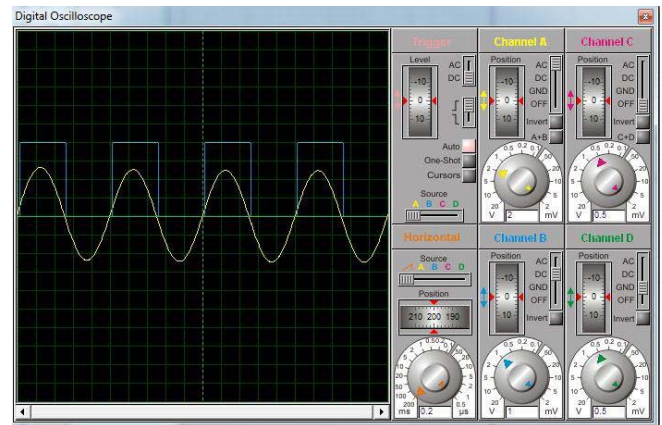


Figure 7 : Zero crossing detector circuit output

IV. SIMULATION & FLOW CHART

The initial stage, the circuits have been designed and simulated in PROTEUS. The circuit have been utilized to detect the power factor using Android application via Bluetooth module. The circuit diagram can be found in figure 8.

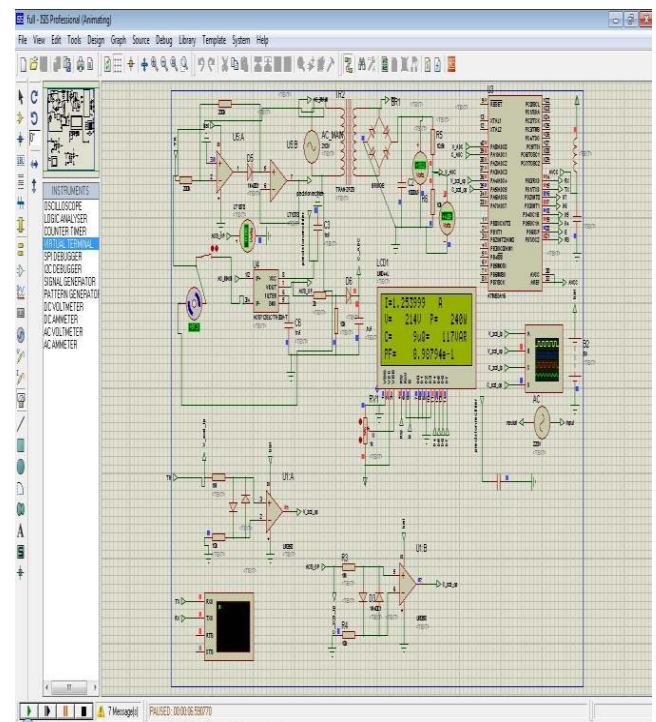


Figure 8 : Circuit Simulation

The output of the Bluetooth Module (Via UART) is shown in Fig 9.

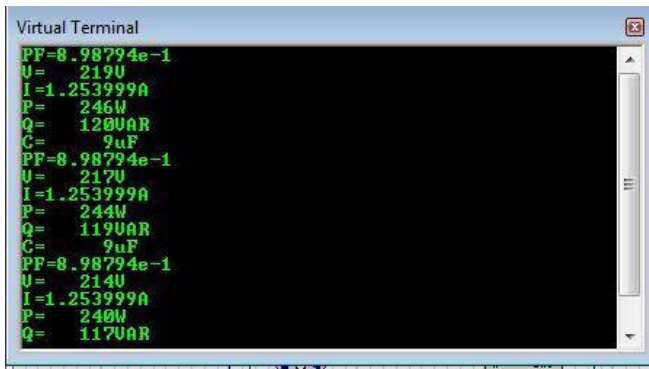


Figure 9 : Bluetooth Module output (Via UART)

The flow chart of the proposed automatic power factor Detection using Android application via Bluetooth module is shown in Fig 10.

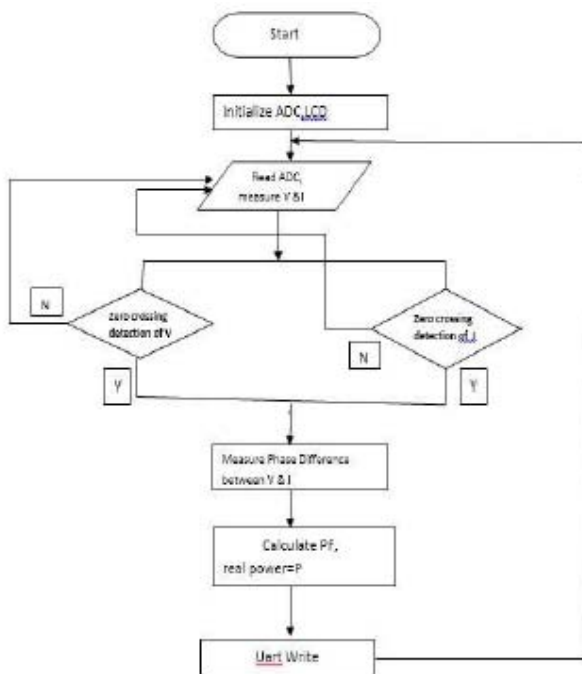


Figure 10 : Flow Chart of the Project

As can be seen from the flow chart, the sequence of operation depicted clearly. The process runs continuously in accordance with obtained logic. V_{rms} and I_{rms} are read by the Microcontroller using ADC ports. After the zero crossing of voltage and current Signals, which are converted to square-waves, are provided to Microcontroller. Power Factor is measured by the Microcontroller from manipulating of capture module for V and I signals. After measuring the Power factor then microcontroller calculate the real power, reactive power, apparent power and value of capacitor. All the measured value are transmitted via UART.

V. HARDWARE IMPLEMENTATION

In reference to figure 1, the transmitting and receiving side can be described as follows:

a) Transmitting side

Heart of the project is the microcontroller ATMEGA 16. For measuring the line Voltage in this project we have used a step down transformer (220/12V) to converting the line voltage from 220V to 12V. Then, a bridge rectifier has been used to converting the 12 V ac to 12 V dc; after that, voltage divider have been applied to converts the 12 V to 5 V because the microcontroller works at maximum 5 V after that we connect it into a microcontroller pins. For current here we used a Hall Effect current sensor and connect the sensor output to another microcontroller pin. From this two pin the microcontroller measures the line voltage and current through ADC. For measuring zero crossing of voltage and current here we used a current transformer and potential transformer and the output ZCD are connected with microcontroller pins and microcontroller measures the phase angle between voltage and current. the Bluetooth module power is given from external power source (4V battery). Bluetooth module communicates with atmega16 through UART. RXD of Bluetooth module is connected with TXD of atmega16 and TXD of Bluetooth module is connected to RXD of atmega16.

b) Receiving side

In receiver Side an Android Phone is available which is connected with transmitting side via Bluetooth Module CI Android Apps [6]. The apps can communicate with Bluetooth Module HC-06. The password of the module is 1234. The communication protocol is UART and baud rate is 9600[4].

In view of the descriptions above, the implemented hardware can be found in figure 10.



Figure 11 : Implemented Hardware model

As viewed from figure 11, Automatic Power factor detection system to be found. In figure 12, the corresponding representation appears in the LCD display with the Voltage, Current, Power factor, real power, reactive power and the value of Capacitor. The view from Android Phone is also shown in figure 12.



Figure 12 : Android Phone output

VI. FUTURE PROSPECTS

In The view of a wide and short range of possibilities on the basis of Bluetooth based power factor Detection system, a few has been depicted below:

- Improvements to human-machine interface.
- Load controlling.
- Load status checking and fault detection.
- Capacitor Switching etc.

Also this project work has not been tested on synchronous motor because of the requirement of considerable expense. It needs the further enhancement of the system. Finance is a critical issue for further enhancement.

VII. CONCLUSION

This Project has proposed the advanced method of the power factor Detection by using the Atmega16 and Android application via Bluetooth module which has the many advantages over the various conventional methods of the Power factor compensation. The microcontroller always monitor power factor, voltage and current and it always send the current status of the load via Bluetooth module. This project gives more reliable and user friendly power factor detection. Thus we have presented the Possible advanced method for the detection of the power factor.

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Four Ports Wideband Pattern Diversity MIMO Antenna

By Ramling P. Manurkar & Veeresh G. Kasabegoudar

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Abstract- In this paper a broadband four ports MIMO antenna with pattern diversity is presented. To obtain pattern diversity, four microstrip feeding lines are printed on one side of the substrate where as the modified ground plane is printed on the other side. These microstrip lines generate orthogonal radiation patterns. In annular slot four shorts are placed between the microstrip lines to maintain isolation more than 25dB. The antenna operates in the range of frequencies from 2.3GHz to 12.6GHz (nearly 139%) which covers FCC defined wireless applications. Besides FCC, the proposed antenna covers WLAN and Wi-Max applications. A design and optimization of proposed geometry is done in Ansoft's HFSS 13. Simulated and measured reflection coefficients, coupling coefficients and radiation patterns are presented.

Keywords: *broadband antenna, pattern diversity antenna, slot antenna and ultra wide band (UWB) antenna.*

GJRE-F Classification : FOR Code: 090699



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Four Ports Wideband Pattern Diversity MIMO Antenna

Ramling P. Manurkar^a & Veeresh G. Kasabegoudar^σ

Abstract- In this paper a broadband four ports MIMO antenna with pattern diversity is presented. To obtain pattern diversity, four microstrip feeding lines are printed on one side of the substrate where as the modified ground plane is printed on the other side. These microstrip lines generate orthogonal radiation patterns. In annular slot four shorts are placed between the microstrip lines to maintain isolation more than 25dB. The antenna operates in the range of frequencies from 2.3GHz to 12.6GHz (nearly 139%) which covers FCC defined wireless applications. Besides FCC, the proposed antenna covers WLAN and Wi-Max applications. A design and optimization of proposed geometry is done in Ansoft's HFSS 13. Simulated and measured reflection coefficients, coupling coefficients and radiation patterns are presented.

Keywords: broadband antenna, pattern diversity antenna, slot antenna and ultra wide band (UWB) antenna.

I. INTRODUCTION

One important challenge to design a compact antenna for multiple applications is smaller and thinner terminals with increasing operating frequency bands and performance. Bandwidth enhancement is major requirement to design printed antenna for different applications, like WLAN, cellular and cordless phones. These applications are used in urban environments where multipath propagation and fading may occur.

To mitigate channel fading and improve transmission quality, antenna diversity technique is efficient. There are several types of diversity, such as pattern, polarization and space diversity has been used [1, 2]. The signals are combined in several ways to optimize the output signal power or signal to noise ration. In diversity method, selection includes combining of signals, where highest SNR is selected. The signals from all branches are compared with respective received signal [3].

The correlation between received signals shows the diversity performance [4]. If correlation is high then the combining efficiency is reduced. In spatial diversity method, decoupling between signals is achieved by maintaining separation between antennas. This scheme is difficult to implement in mobile handsets because of space restriction. To overcome this drawback pattern diversity and polarization diversity schemes are

investigated [5, 6]. These techniques use two or more co-located antennas with different radiation patterns. To receive signal horizontally and vertically, dipole or microstrip antennas are used [7]. Microstrip antennas are widely used for diversity schemes due to their low cost. However, these suffer from narrow bandwidth. To extend bandwidth, many solutions are proposed including impedance matching networks, substrates with low dielectric constants or parasitic patches on the top of main patch [8] etc.

Slot antennas traditionally demonstrated a large bandwidth than microstrip antennas. For example in [9], slot with two feeding ports for pattern diversity applications is used. Pattern diversity antennas have been proposed in many literatures and carried out with different methods. In [10] two monopoles were used to provide pattern diversity. The T-shaped network was used to feed antenna with operating frequency range of 1790-2200 MHz, was achieved. In [11], a planar dual port diversity antenna was presented, which operates with broadside and conical radiation patterns in H-plane. In this geometry, operating bandwidth of 50% is achieved for diversity. In [12] pattern diversity is achieved by dual ports microstrip antenna with two shorts to maintain isolation above 15dB between these two ports. The annular slot is used for pattern diversity. The total bandwidth is increased up to 120%.

In this paper, we extended the work reported in [12]. Here, we designed annular ring slot with four ports. Furthermore, geometry of [12] uses air gap which makes the whole geometry as delicate and difficult to assemble. To overcome this FR4 material is used to fabricate the proposed prototype. Also, the substrate is easy to handle as compared to air dielectric material. The simulated and measured return loss, isolation between ports, and radiation patterns are presented. Antenna geometry is presented in Section 2. Section 3 presents geometry optimization procedure. Experimental results and discussions are covered in Section 4. Finally, the work is concluded in Section 5.

II. ANTENNA DESIGN

In this section, the annular slot antenna which generates four orthogonal patterns is proposed for diversity applications in multipath fading environment. The proposed annular slot microstrip antenna, wideband behavior was achieved. To obtain wideband with pattern diversity, two more ports have been inserted

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in [12] geometry as shown in Figure 1. Four shorts are placed in antenna, in order each shorts between feed lines. These shorts are placed to obtain high isolation between ports. Good isolation between ports is achieved when single short is placed between ports.

The proposed antenna is printed on FR4 dielectric material with thickness of 0.8 mm. The annular slot is etched on square chassis of 100 x 100 mm². The antenna is fed with four microstrip lines printed on

bottom side of substrate and shown in Figure 1. The microstrip feed lines terminated with sub-tuned patches to match the antenna to characteristics impedance of microstrip line.

Table 1 : Dimensions of the optimized geometry

Parameter	L	R_{in}	R_{out}	L_{feed}	L_1	W_{feed}	W_1
Value (mm)	100	12	26	25.6	9.4	1.2	7.2

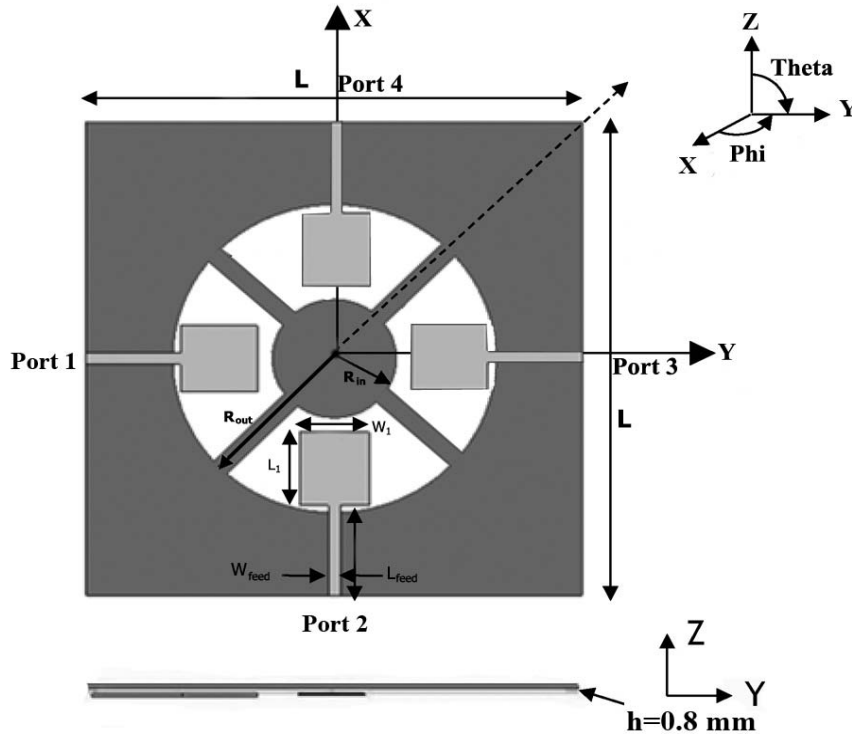


Figure 1 : The proposed geometry of annular slot antenna (light grey: bottom view; darker grey: top view)

III. GEOMETRY OPTIMIZATION AND DISCUSSIONS

In this section parametric study is conducted by optimizing the proposed geometry of antenna. The key design parameters used for optimization are inner radius R_{in} , patch length L_1 , patch width W_1 , and feed width W_{feed} . All simulations were carried out in HFSS 13 software. Here the parameters of R_{in} , L_1 , W_1 , W_{feed} are considered (one at a time keeping other parameters constant) to investigate the effect of each of these parameters on the antenna's performance.

a) Effect of Inner Radius

The inner radius (R_{in}) of the circle is varied in steps of 1 mm from 10 mm to 13 mm by keeping all other parameters constant. The simulated results of reflection coefficient of antenna with varying R_{in} are shown in Figure 2. From these characteristics it may be noticed that the good bandwidth of antenna is obtained for $R_{in}=12$ mm.

b) Effect of Patch Length

In this study, keeping inner radius (R_{in}) constant, the patch length L_1 was varied (9mm to 9.8mm in step of 0.2mm) to investigate its effect on the antenna geometry. Results of this study are presented in Figure 3. These results indicate that the geometry offers good bandwidth for $L_1=9.4$ mm.

c) Effect of Patch Width

In another effort, the patch width W_1 is varied (6.8mm to 7.6mm) in steps of 0.2mm. The simulated reflection coefficient characteristics of this study are shown in Figure 4. From these simulated results, it may be noticed that optimum bandwidth can achieved for $W_1=7.2$ mm.

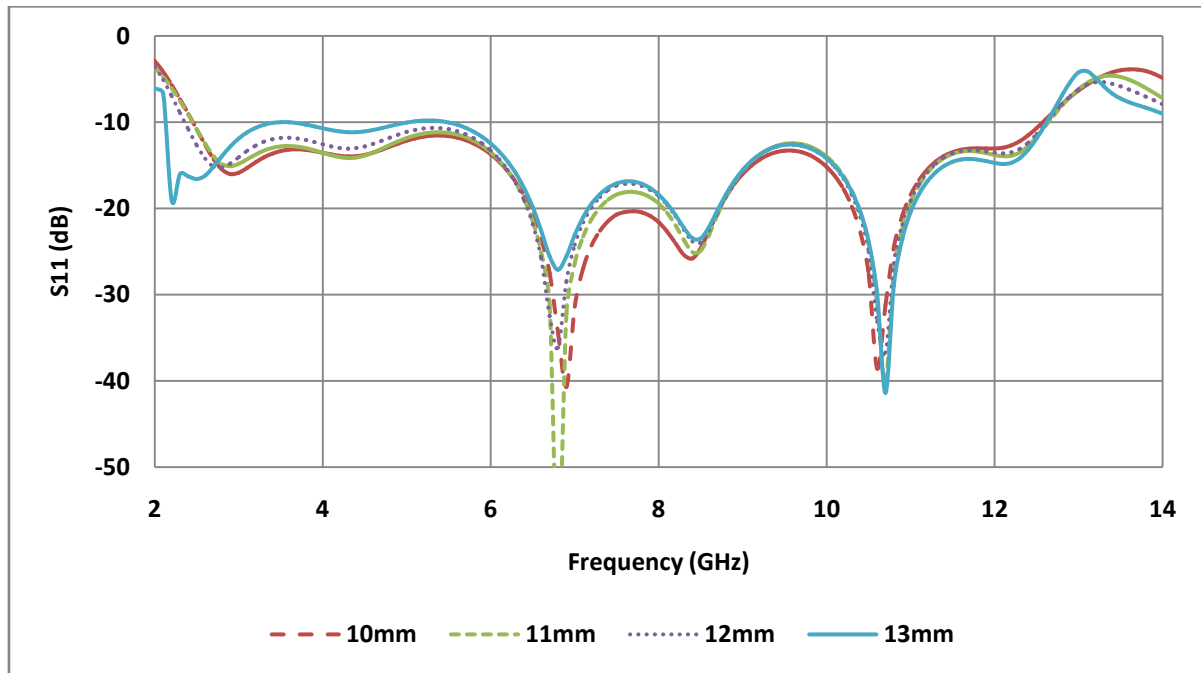


Figure 2 : The effect of inner radius R_{in} on reflection coefficient characteristics.

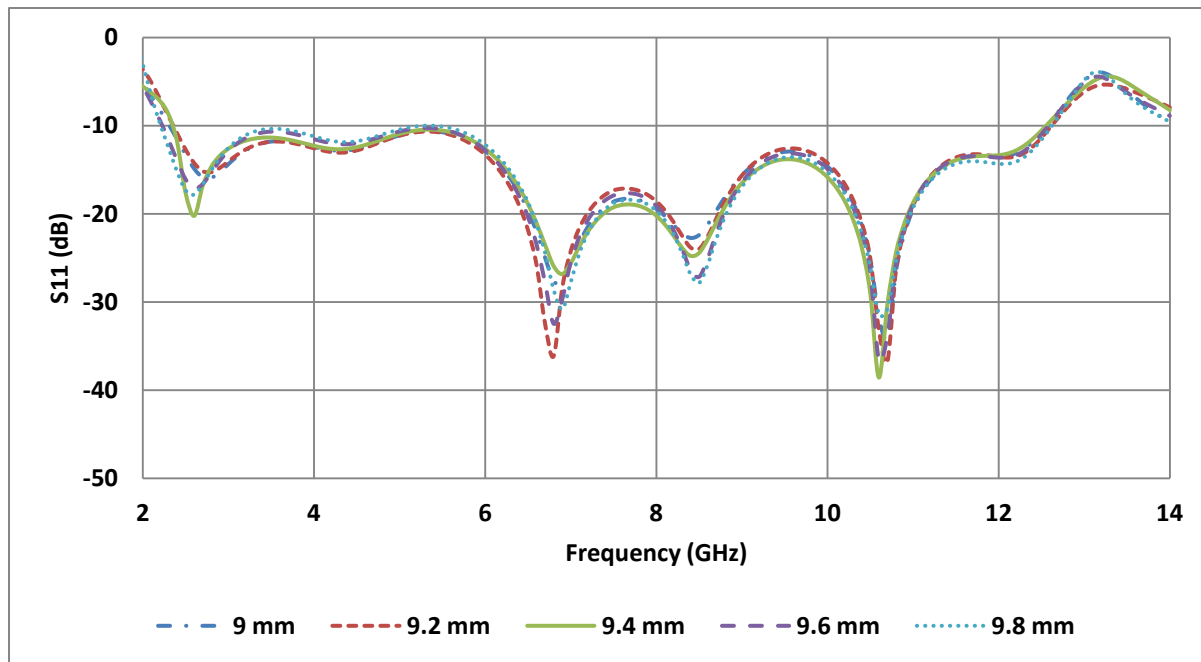


Figure 3 : The effect of patch length L_1 on reflection coefficient characteristics

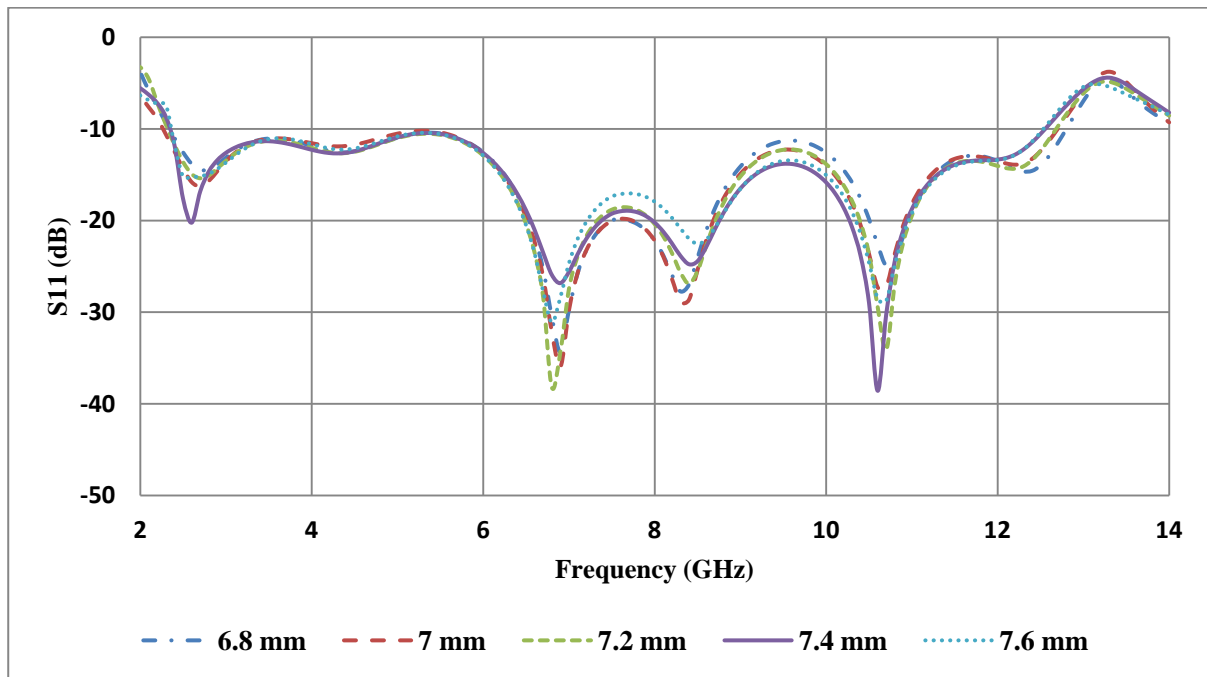


Figure 4 : The effect of patch width W_1 on reflection coefficient characteristics.

d) Effect of Feed Width

Finally, keeping all parameters discussed in earlier subsections constant, feed width (W_{feed}) of antenna was varied from 1mm to 1.4mm in steps of

0.2mm. The simulated characteristics for various feed widths are presented in Figure 5. By observing these characteristics it may be noted that the bandwidth of antenna is optimum for feed width equal to 1.2mm.

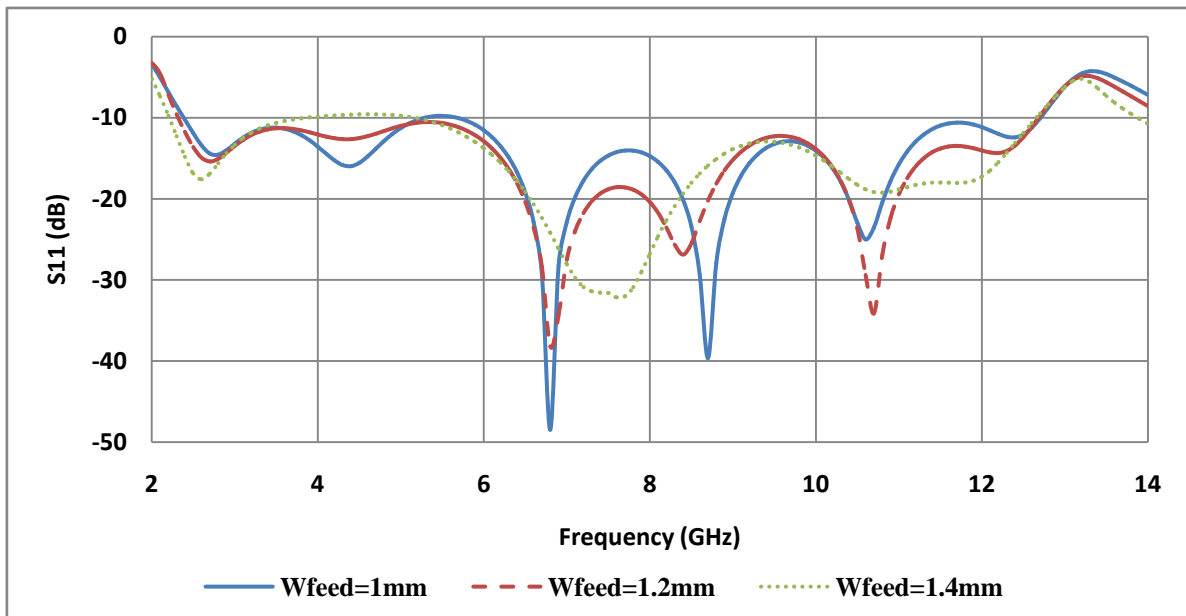
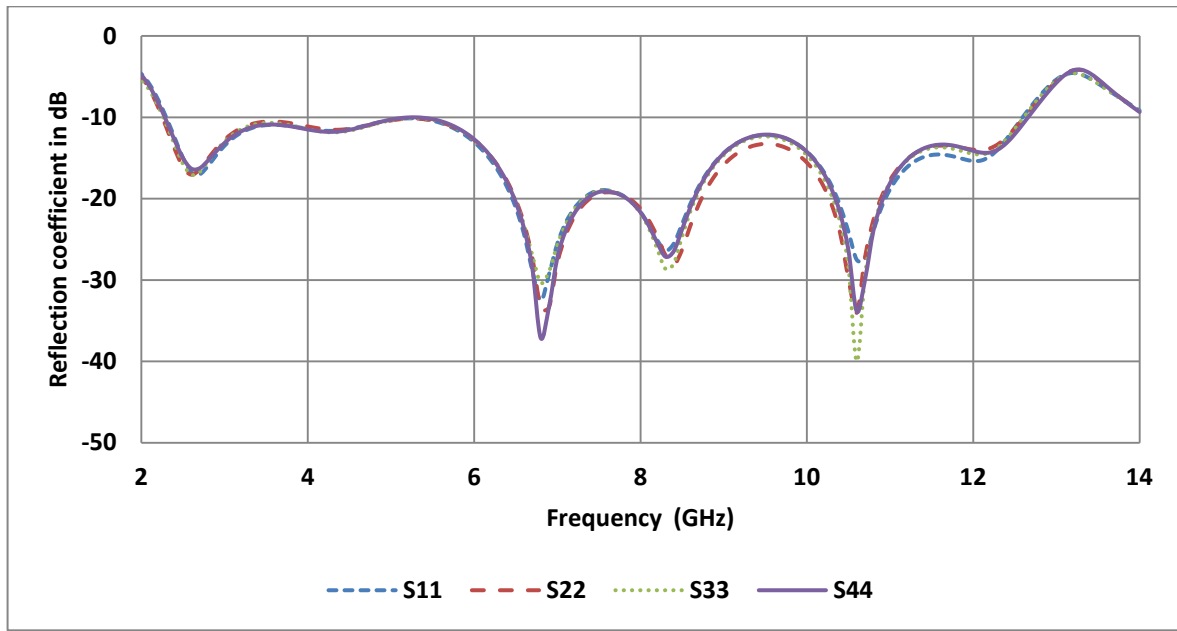


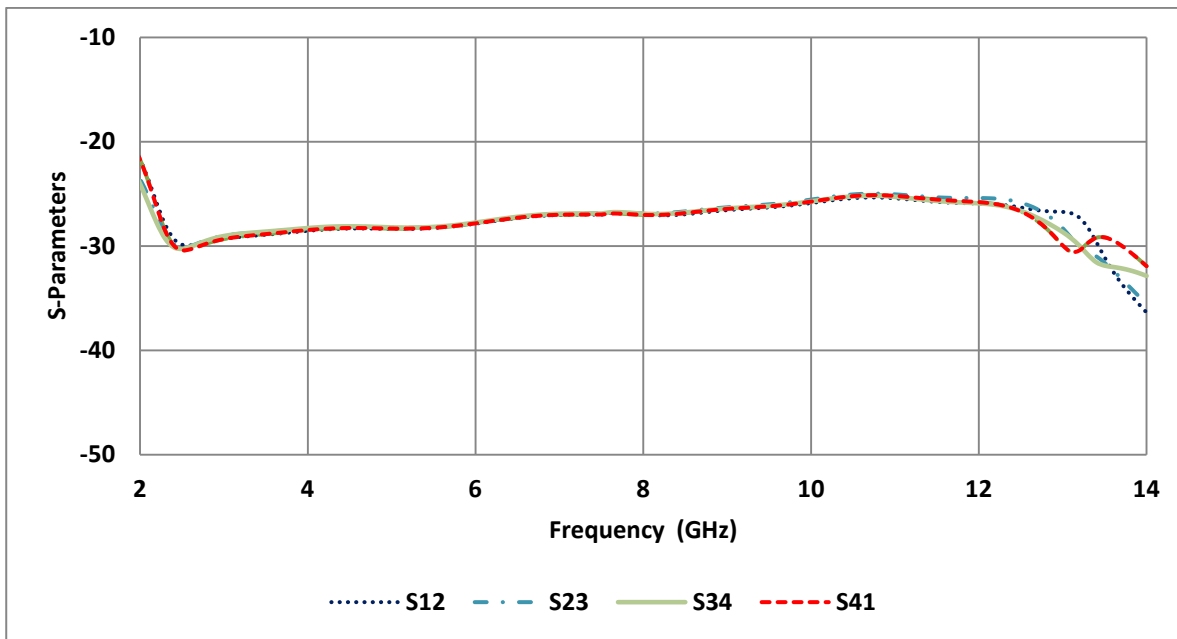
Figure 5 : The effect of feed width W_{feed} on reflection coefficient

From the parametric study conducted, it may be noted that the geometry with its dimensions listed in Table 1 offers optimum performance. The reflection coefficient characteristics and mutual coupling coefficients of the proposed optimum geometry are depicted in Figure 6. From these results it may be noted that the antenna offers 139% impedance bandwidth.

Also, the mutual coupling is well below -20dB throughout the band of operation.



(a)



(b)

Figure 6 : Simulated results (a) Reflection coefficient (b) Coupling coefficient

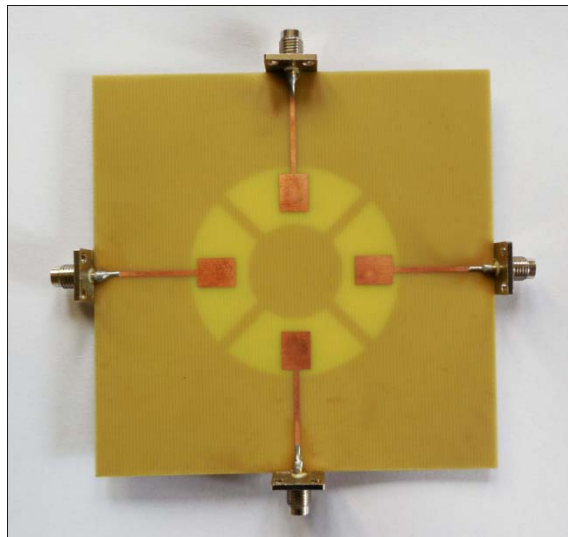
IV. EXPERIMENTAL VALIDATION OF THE GEOMETRY AND DISCUSSIONS

The proposed geometry shown in Figure 1 with its optimized dimensions listed in Table 1 is fabricated on FR4 substrate having dielectric constant of 4.4 and thickness of 0.8mm. The photograph of fabricated prototype is shown in Figure 7. In Figure 8, the setup of measurement of reflection coefficient of antenna in real environment is shown. Reflection coefficient characteristics of measured results are compared with

simulated values in Figure 9. In Figure 9 only S_{11} is presented as all ports are symmetrical. The measured results slightly mismatch with the simulated values which may be due to inaccuracies in the fabricated prototype. Simulated radiation patterns at different frequencies in the operating band are presented in Figure 10. From these patterns it may be noted that the patterns remain nearly stable across the band of operation. Also, omni-directional patterns are obtained in the H-plane suitable for broadcasting applications.



(a)



(b)

Figure 7 : Photograph of fabricated prototype (a) Front side (b) Back side



Figure 8 : Photograph of measurement setup

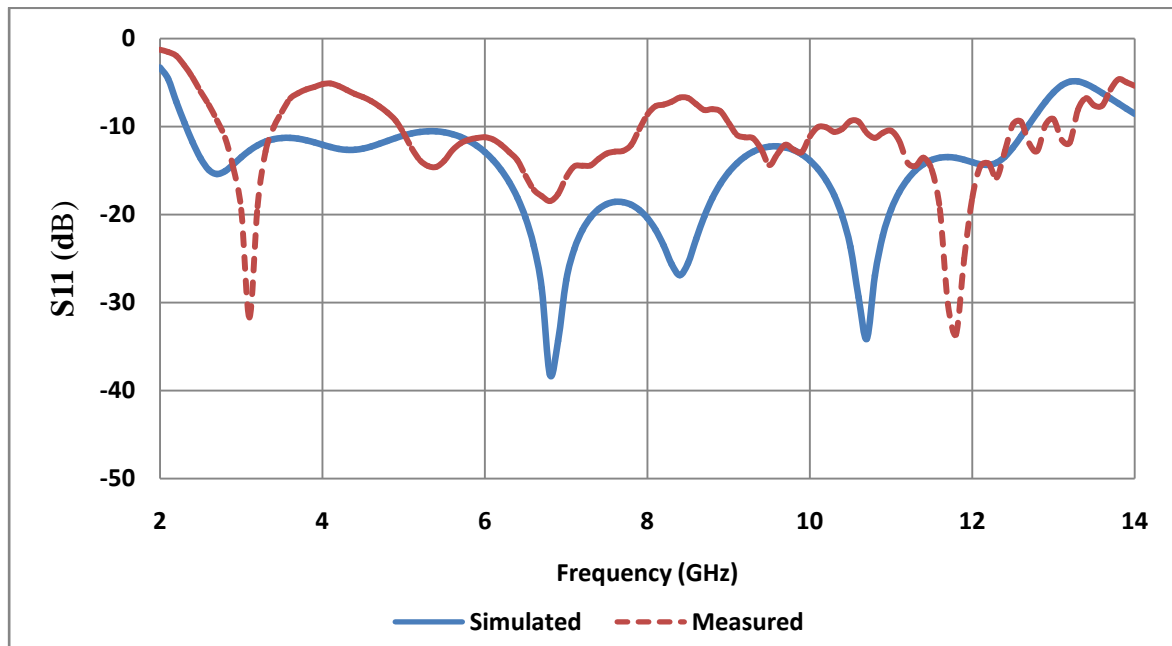
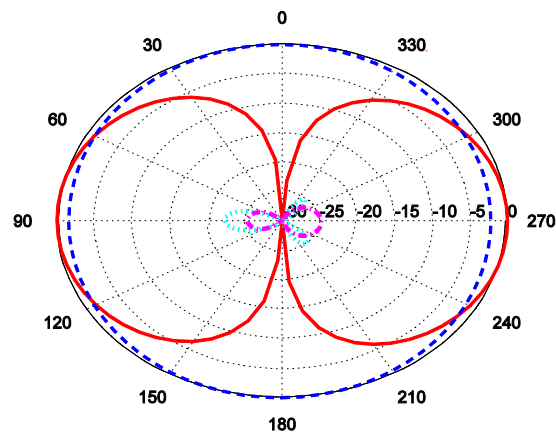
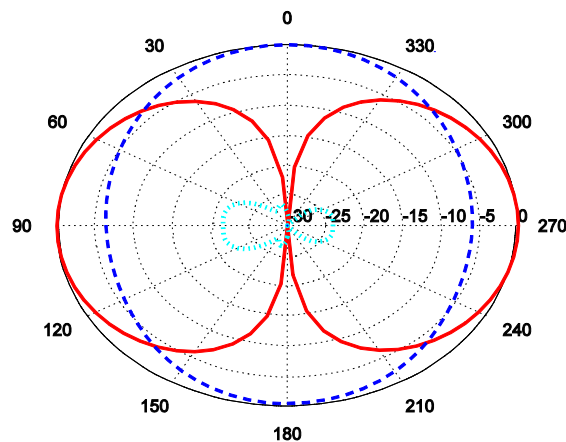


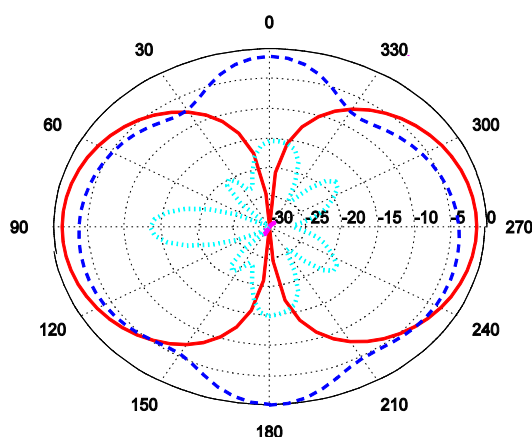
Figure 9 : Comparison of simulated and measured S_{11}



(a) Radiation patterns at 2.7GHz.



(b) Radiation patterns at 6.8GHz.



(c) Radiation patterns at 10.7GHz

Figure 10 : Radiation patterns at different frequencies in the operating band (Red (solid-line): E co-poln.; Blue (dashed-line): H_co-poln.; Cyan (dotted-line): E cross-poln.; Magenta (dash-dot-line): H cross-poln.)

V. CONCLUSIONS

Four ports wideband pattern diversity antenna has been presented. The antenna is etched on chassis of $100 \times 100 \text{ mm}^2$ fed with four microstrip lines printed on backside of the substrate. Four shorts are inserted to increase the isolation between the ports & to improve the performance of antenna. Various characteristics of the antenna are presented and satisfactory performance was achieved. The antenna covers the FCC defined UWB band of frequencies. The proposed antenna offers an impedance bandwidth of 139% in the frequency range of 2.3GHz to 12.6GHz. Also, the geometry produces stable and omni-directional patterns with good gain over the band of operation. Besides this the antenna exhibits good performance for all four ports, and hence the geometry is suitable for diversity applications.

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- Make a decision if each premise is supported, discarded, or if you cannot make a conclusion with assurance. Do not just dismiss a study or part of a study as "uncertain."
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- You may propose future guidelines, such as how the experiment might be personalized to accomplish a new idea.
- Give details all of your remarks as much as possible, focus on mechanisms.
- Make a decision if the tentative design sufficiently addressed the theory, and whether or not it was correctly restricted.
- Try to present substitute explanations if sensible alternatives be present.
- One research will not counter an overall question, so maintain the large picture in mind, where do you go next? The best studies unlock new avenues of study. What questions remain?
- Recommendations for detailed papers will offer supplementary suggestions.

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- When you refer to information, differentiate data generated by your own studies from available information
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<i>References</i>	Complete and correct format, well organized	Beside the point, Incomplete	Wrong format and structuring



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