# Average Current Control Mode Boost Converter for the Tuning of Total Harmonic Distortion & Power Factor Correction using PSIM Kashif Habib<sup>1</sup> <sup>1</sup> Northwestern Polytechnical University *Received: 12 December 2013 Accepted: 4 January 2014 Published: 15 January 2014*

### 8 Abstract

The aim of this paper is to investigate the power factor correction (PFC) of Boost Converter 9 under an average current-mode control. Boost converter topology is used to accomplish this 10 active power-factor correction in many discontinues/ continuous modes. The boost converter 11 is used usually it is easy to implement and works well. In this paper comparative evaluation of 12 different techniques for harmonic reduction in input current of ac-dc converter is presented. 13 Converters employing as side switching and boost converter is simulated in PSIM Software. 14 Average current tracks the current program with a high degree of accuracy. This is especially 15 important in high power factor pre-regulators, enabling less than 5 16

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18 Index terms— PSIM; average current control; power factor correction; total harmonic distortion (THD).

### <sup>19</sup> 1 INTRODUCTION

C-DC converters are used in adjustable speed drives, SMPS, UPS etc. Most of power Electronics (PE) system which get connected to AC utility mains use diode rectifier at the input. The non-linear nature of diode rectifier causes significant line current harmonic generation, thus, they degrade power quality, increases losses, failure of some crucial medical equipment and so on. Therefore, stringent international standard are imposed. Hence, harmonic reduction circuits are incorporated in PE system [1].

Earlier expensive bulky inductor and capacitor were installed [2] but they effectively eliminated certain harmonic. Active power line conditioners (APLC) used for harmonic reduction are generally hard switched, which result in low efficiency, low EMI, high component stress etc. Soft switched resonant converter are also used and are usually operated in variable frequency mode and thus component are required to be designed at lowest operating frequency. Active clamped technique is well known for zero voltage switching (ZVS) operation in various converters. Boost converter topology in continues conduction mode (CCM) is used in medium power AC/DC converter, as it gives near unity power factor at ac input [3,4].

Power-factor-correction (PFC) converters are widely used in power supplies for pre-regulating of power factor. 32 Generally speaking, any type of switching converters can be the candidate for PFC purpose [5][6][7][8]. But in 33 practical the Boost converter has been the favorable and popular choice when taking into account the factor of 34 35 current stress and efficiency. As a typical nonlinear circuit system, PFC Boost converters are recently revealed to 36 exhibit fast-scale instability, such as bifurcation and chaos operation, over the time of line cycle. These complex 37 behaviors implying instability should be avoided from the viewpoint of traditional design principles, which can 38 be realized by the changing of circuit parameters, or enclosing the accessional control method when the circuit parameters are fixed. The basic practical requirement for power supplies is to regulate output voltage. Moreover, 39 this requirement has to be combined with that of power-factor-correction (PFC) in the design of most practical 40 power supplies. Defined as the ratio of the active power to the apparent power, the power factor represents a useful 41 measure of the overall quality level of satisfaction of power supplies and systems in such areas of performance as 42 harmonic distortion and electromagnetic interference. Generally speaking, any type of switching converters can 43

<sup>44</sup> be chosen as a PFC stage. In practice, taking into account the current stress and efficiency, the boost converter
<sup>45</sup> has been a favorable and popular choice. The discontinuous conduction mode of operation has the obvious
<sup>46</sup> advantage of simplicity since no additional control is required [9].

In a conventional switching power supply employing a buck derived topology, the inductor is in the output.
Current mode control then is actually output current control, resulting in many performance advantages
[10][11][12]. On the other hand, in a high power factor pre-regulator using the boost topology, theA Global
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inductor is in the input. Current mode control then controls input current, allowing it to be easily conformed 51 to the desired sinusoidal wave shape. In high power factor boost pre-regulators the peak/average error is very 52 serious because it causes distortion of the input current waveform. While the peak current follows the desired 53 sine wave current program, the average current does not. The peak/average error becomes much worse at lower 54 current levels, especially when the inductor current becomes discontinuous as the sine wave approaches zero 55 every half cycle. To achieve low distortion, the peak/average error must be small. This requires a large inductor 56 to make the ripple current small. The resulting shallow inductor current ramp makes the already poor noise 57 immunity much worse. The average current mode method can be used to sense and control the current in any 58 circuit branch. Thus it can control input current accurately with buck and fly back topologies, and can control 59 60 output current with boost and fly back topologies.

### 61 **2** II.

# 62 3 DESIGN OF SYSTEM

I have used average current control Boost Converter for the improvement of power factor and total harmonic 63 distortion. The boost converter is a high efficiency step-up DC/DC switching converter. The converter uses 64 a transistor switch, typically a MOSFET, to pulse width modulate the voltage into an inductor. Rectangular 65 pulses of voltage into an inductor result in a triangular current waveform. For this discussion we assume that 66 the converter is in the continuous mode, meaning that the inductor's current never goes to zero. Some formulas 67 and mathematical notations of Boost Converter are shown below in table 1. The average current mode control 68 method is feedback control for current. I have used two PI controllers to stabilize the system. After using this 69 average current control method, I have taken good results. 70

# 71 4 III. SIMULATION & RESULTS USING PSIM SOFTWARE

I have selected the PSIM software for simulation. Firstly I have explained only the bridge rectifier. Then calculate its THD, which is good because no energy using equipments in the circuit so it's THD is very good. Then I have shown the bridge rectifier using boost converter. The results show that its THD is very high so I have to improve this THD. For this purpose I have selected the average current controlled method. The result shows clearly that a lot of ripples in the waveform. So it must be sure that its THD must be very high. The THD data is shown below in figure (6). We can see clearly that its THD is more than 60% and we see a lot of ripples in the requert the ripples and make the THD around 5%. So for this we have to design a the the the the test of the test of the test.

79 controller that gives us good results and improved power factor.

# 80 5 c) Average Current Control Method using Boost Converter

The circuit diagram of Average Current Control method using Boost Converter is shown below in figure (7). I 81 have worked on PSIM Software. The value of each component is shown in the circuit. I am using 220 RMS in the 82 input and getting 400V in the output. I have arranged the values according to the circuit like duty cycle of 0.4 83 and the values of PI controller are set according to the circuit requirement. The table shows the basic components 84 of the circuit Diagram. It can be seen that there is no ripple in the input current, so its THD must be very 85 good. The THD of input current is shown below in figure (11). In the average current control method, we use 86 a feedback circuit diagram as we can see in figure (7). In the feedback circuit diagram, we have the comparison 87 analysis of Inductor Current and Reference Current as shown in figure (12). After applying the variable load, 88 the THD is shown in figure (16) this is good around 4%. IV. 89

## 90 6 CONCLUSION

I have used the Software PSIM, which helped me for the measurement of THD and Power Factor. Firstly I show the results of open loop uncontrolled rectifier and then shows the average current control method. The average current control method improves the results (THD and Power Factor). In the results of uncontrolled rectifier, we can see that harmonics are very high so our task is to reduce the harmonics. I used the close loop controlled rectification and arranging the PI controllers to get the good results. I have also shown the comparison of Inductor current and the reference current which is the comparison of rectified scaled voltage and the output DC voltage. I have also shown the transient analysis of average current control method, which also shows good

 $_{98}$   $\,$  results. So I got the good THD which is 4.45%.



Figure 1: Figure 1 :

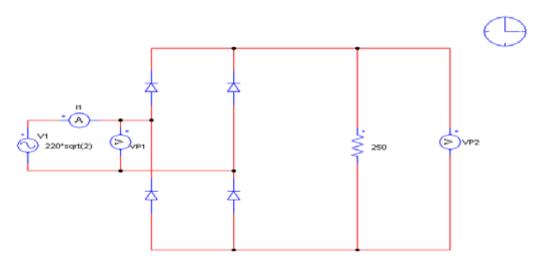


Figure 2:

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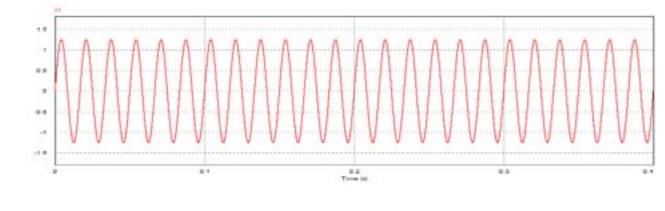


Figure 3: Figure 2 :

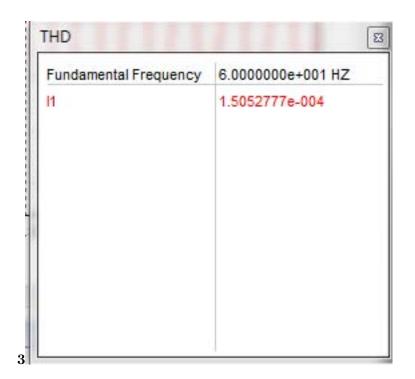


Figure 4: Figure 3 :

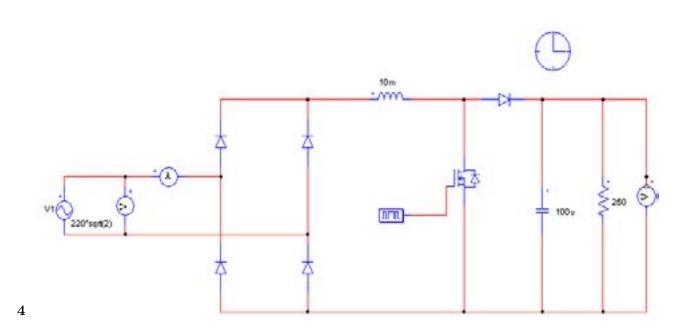
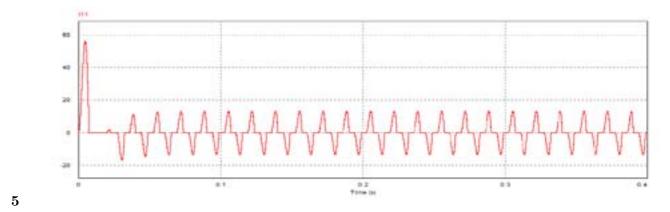
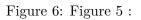


Figure 5: Figure 4 :





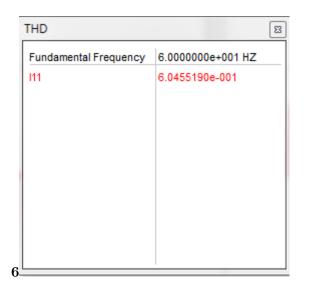


Figure 7: Figure 6 :

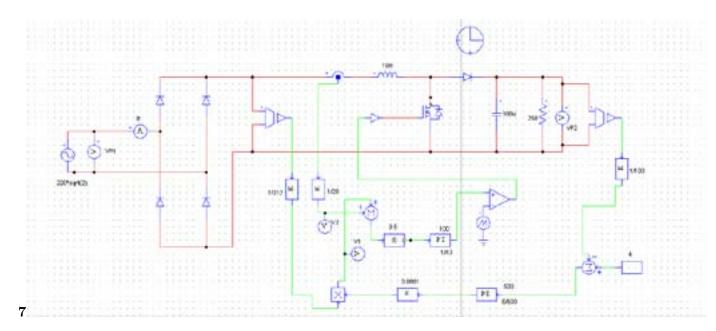
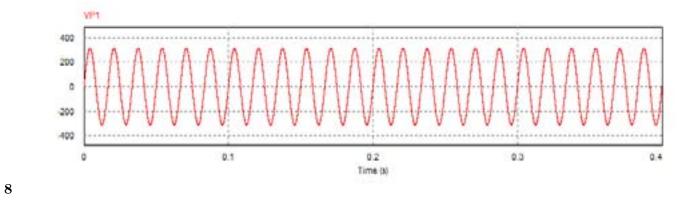
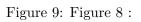


Figure 8: Figure 7 :





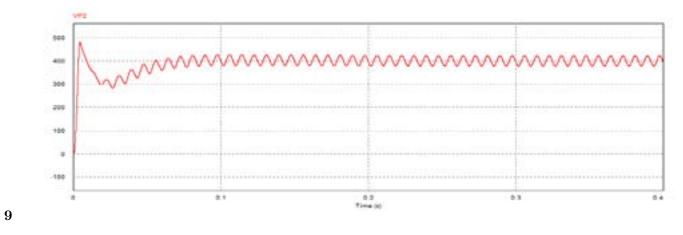


Figure 10: Figure 9 :

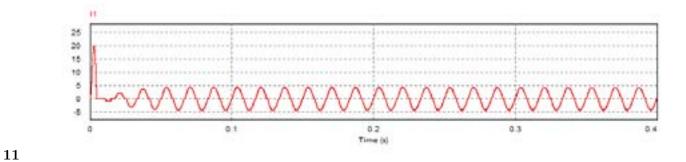


Figure 11: Figure 11:

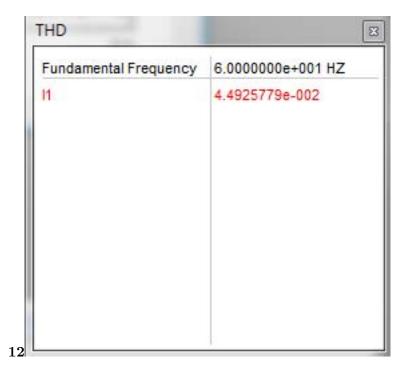
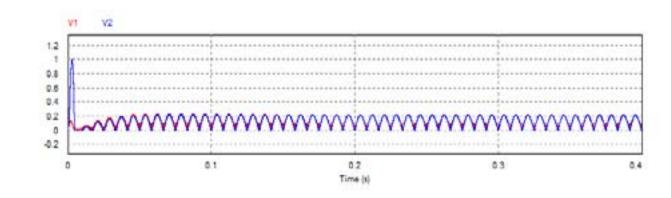


Figure 12: Figure 12:



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Figure 13: Figure 13 :

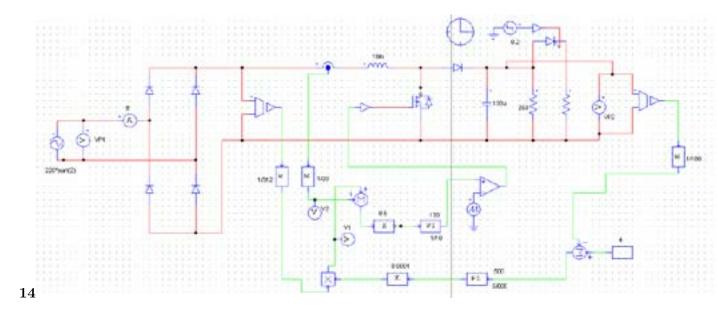
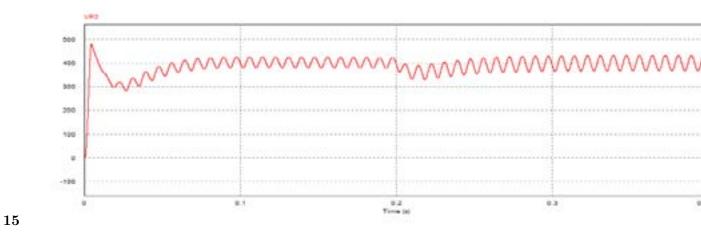
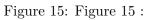


Figure 14: Figure 14 :





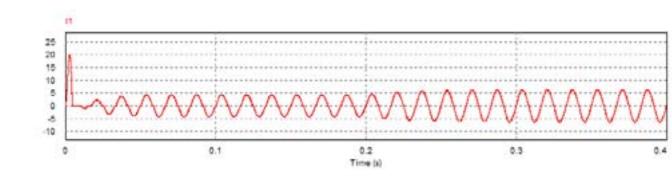




Figure 16: Figure 16 :

Fundamental Frequency	6.0000000e+001 HZ
11	3.7865733e-002

Figure 17: Figure 17 :

1

Components/ Parameters	Formulas
Peak inductor	
current	
Min inductor	
current	
Ripple Current	(ii? = pk? o) i
	i
Ripple Current	
Ratio to Average	r / ave i i = ?
Current	
Off Duty Cycle	

[Note: pk i o i]

Figure 18: Table 1 :

 $\mathbf{2}$ 

Components/ Parameters	Values
Input Voltage	220V RMS
Duty cycle	0.4
Inductor	$10 \mathrm{mH}$
Capacitor	100  uF
Resistor	250 ?
Switching Frequency	$100 \mathrm{~KHz}$
Reference Voltage	400 V

Figure 19: Table 2 :

99 V. <sup>1</sup>

 $<sup>^1 \</sup>odot$  2014 Global Journals Inc. (US)

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- 105 [Global Journal of Researches in Engineering], Global Journal of Researches in Engineering
- 106 [Review on Power Quality Solution Technology B. Singh,G. Bhuvaneswari1 S.R. Arya Asian Power Electronics Journal (2012)]
- 107 , Review on Power Quality Solution Technology B. Singh, G. Bhuvaneswari1 S.R. Arya Asian Power Electronics
   108 Journal Dec 2012. 6 (2) .
- 109 [Deepshikha Jaiswal et al. (2012)] 'Analysis of Bridgeless PFC Boost Converter'. Deepshikha Jaiswal , K P Mr
- , Dr A N Singh , Tiwari . International Journal of Engineering Research & Technology (IJERT) 2278-0181.
   July -2012. 1.
- [Dixon ()] Average Current Mode Control of Switching Power Supplies, L H Dixon . 1990. (Unitrode Power
   Supply Design Seminar)
- [Average Current Mode Control of Switching Power Supplies Lloyd Dixon] Average Current Mode Control of
   Switching Power Supplies Lloyd Dixon,
- [Zhang ()] Bidirectional DC-DC Power Converter Design Optimization, Modeling and Control, Junhong Zhang
   . 2008. Virginia Polytechnic Institute and State University
- <sup>118</sup> [Zhou et al.] Blacksburg, design trade-offs in continuous current mode controlled boost power factor correction <sup>119</sup> circuits, C Zhou, M M Jovanoviac, Delta Power Electronics Lab, Inc.
- 120 [Sun et al. ()] Cedar Rapids, IN, Modelling and practical design issues for average current control, Applied
- Power Electronics Conference and xposition, J Sun , R M ; Bass , Adv Technol , Center , Collins , Inc . 1999.
   (APEC'99.14 th Annual)
- 123[Tse and Lai (2000)] 'Control of bifurcation in current-programmed DC/DC converters: a reexamination of slope124compensation'. C K Tse , Y M Lai . IEEE ISCAS June 2000. p. .
- [Darly et al. (2011 1st)] S Darly , V Ranjan , K V Bindu , B J Rabi . International Conference, (Anna University,
   Chennai, RREC, Chennai, ICEES) 2011 1st.
- 127 [Fkcat et al.] Design Tool for PFC Converter Implemented in Matlab, Cristian Fkcat , Mihai Crasi , Niculaie
   128 Palaghita . IEEE Transaction.
- 129 [Parillo] Dual Boost High performances Power Factor Correction Systems(PFC), F Parillo.
- [Erickson and Maksimovic ()] Robert W Erickson , Dragan Maksimovic . Fundamentals of Power Electronics,
   2001. Kluwer Academic Publishers. p. 2.
- Brandon et al. ()] 'Inductance Compensation of Multiple Capacitors with Application to common-and
   Differential-Mode Filters'. J Brandon , Timothy C Pierquet , Neugebauer , J David . 37th IEEE Power
- *Electronics Specialists Conference*, (Cambridge, MA Charles Stark Draper Laboratory, Cambridge, MA; Jeju,
   Korea) June 18 -22, 2006. Perreault Laboratory for Electromagnetic and Electronic Systems Massachusetts
   Institute of Technology
- [Hsu et al. ()] 'Modelling and Analysis of Switching DC-to-DC Converters in Constant Frequency Current
   Programmed Mode'. S Hsu , A Brown , L Rensink , R D Middlebrook . *IEEE PESC Proceedings* 1979.
- [Wang et al. ()] 'Operation Principles Of Bi-Directional Full-Bridge Dcdc Converter With Unified Soft-Switching
   Scheme And Soft-Starting Capability'. Kunrong Wang , Fred C Lee , Jason Lai . *IEEE Transaction* 2000.
- [R ()] 'Power-factor-correction in single-phase switching-mode power supplies -an overview'. R. Int. J. Electron
   1994. 77 (5) p. .
- 143 [Orabi et al.] 'Stability Performance of two-stage PFC converters'. M Orabi , T Ninomiya , Dept , Electr .  $\mathscr{B}$
- 144 Electron. Syst. Eng., Kyushu Univ 50. (Industrial Electronics IEEE. Issue-6)
- 145 [Tech (PE) Scholar, 2 Associate Professor Department of EEE An Interleaved Boost Converter with LC Coupled Soft Switching I
- 'Tech (PE) Scholar, 2 Associate Professor Department of EEE'. An Interleaved Boost Converter with LC
- Coupled Soft Switching Mahesh. P1, Srilatha.D2 1M, (Kandukur, Prakasam District, AP, India) Dec. 2013.
   (Prakasam Engineering College)
- [Middlebrook (1985)] 'Topics in Multiple-Loop Regulators and Current-Mode Programming'. R D Middlebrook
   *IEEE PESC Proceedings* June 1985.