Performance Analysis of BLDC Motor Drive using New Simulation Model with Fuzzy and ANFIS Speed Controllers

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Abstract

This paper presents the mathematical model of the brushless DC (BLDC) motor drive fed by the hysteresis current controlled inverter, which is designed using the new switching function concept. The developed simulation model is applied with the fuzzy logic controller (FLC) and adaptive neuro fuzzy inference system (ANFIS) controller as speed controllers to enhance the performance of the BLDC motor drive system. The complementary strengths of FLC and neural networks are combined together to obtain the ANFIS controller. The ANFIS controller is trained by the data of closed loop BLDC motor drive system simulated with PI controller. The ANFIS controller avoids the selection of fuzzy control rules and tuning of membership functions in the manual manner as done in FLC. A comparative study of different performance specifications is proposed between FLC and ANFIS speed controller as applied to the BLDC motor drive system. The simulation results show that the ANFIS controller is more effective as compared to FLC during most of the operating conditions considered.

Index terms— BLDC motor, switching function, FLC, ANFIS controller, performance specifications.

1 Introduction

Ow a days, the brushless dc (BLDC) motors are very widely used in many applications because of high efficiency, high power density, silent operation, long operating life, low maintenance and good dynamic performance [1][2][3][4][5]. Lot of research work is progressing towards the BLDC motor drive due to the increased demand of such systems in the market [6][7][8][9]. However, the use of BLDC motors is restricted due to the complexity involvement in the speed control for applications with the variable speed and varying loads [10][11][12][13]. The classical controllers with fixed gains may not work properly due to the changes in plant dynamics with ageing process, parameter variations and external disturbances [14][15][16].

Fuzzy control is one of the most interesting techniques that can be well applied for the control of imprecise, nonlinear and ill-defined systems [17][18][19] **??**20][21]. The selection of membership functions and the development of fuzzy control rules is a difficult task without the information from the skilled operator. More over the fuzzy control rules could not be defined properly if the skilled operator is unable to give the exact instructions about the process operation. The artificial neural networks have the learning capability which gains the knowledge of a process automatically from the sample input and output relationship of the process [22][23]. This paper introduces the implementation fuzzy logic controller (FLC) and adaptive neuro fuzzy inference system (ANFIS) controller for the speed control of BLDC motor drive system to improve the performance at different operating conditions. This paper is organized in the following manner. Section II illustrates the description of BLDC motor drive that consists of speed controller, current controller, BLDC motor and inverter. Section III presents the simulation results of the drive with FLC as speed controller. Section IV explains the application of ANFIS speed controller to the BLDC motor drive system along with the simulation results. Section V gives the comparative performance of BLDC motor drive for different operating conditions between FLC and ANFIS controller. Finally the main concluding remarks are given in section VI.

2 II.

3 Description of B Motor Drive System

The block diagram of BLDC motor drive system with the combination of FLC and the neural network as speed controller is shown in figure 1. The drive system consists of BLDC motor, rotor position sensor, combined FLC and neural network known as ANFIS controller and three phase voltage source inverter.

There are two basic control loops in the description of the drive system known as speed control loop and current control loop [24][25]. The speed control loop is the outer control loop which consists of BLDC motor, rotor position sensor, speed controller, reference current generator, hysteresis controller and three phase voltage source inverter. The current control loop is the inner control loop which consists of reference current generator, hysteresis current controller and three phase voltage source inverter. The back electro motive force (EMF) wave forms of BLDC motor are in trapezoidal shape. These trapezoidal waveforms for all the three phases are built based on the rotor position (?). sudden load of 0.662 N-m is applied at 0.05 second and withdrawn at 0.12 second. The motor speed is changed at 0.15 second to a value equal to the set speed but in the opposite direction.

4 LDC

5 Implementation of Motor Drive with A Controller

The properties of both fuzzy control and artificial neural networks are combined to produce the ANFIS controller. The FLC is provided with the learning capability by the structure of neural network that helps the FLC to select the best possible ranges for the input and output membership functions that match the sample input and output data. The response of BLDC motor drive with the proportional-integral (PI) controller is used as sample input and output data to train FLC. The basic structure of ANFIS network obtained through the MATLAB is shown in figure 11. The trained membership functions with the best possible ranges for error (e) and change in error (Î?"e) are shown in figure 12. The chosen fuzzy inference system for FLC is Takagi-Sugeno-Kang type of fuzzy inference system. The selected membership functions are the generalized bell shaped one. The parameters of membership functions are tuned by both the back propagation and least squares method through the neural networks structure. The structure of neural network is helping in tuning and selection process of membership functions and fuzzy control rules respectively thereby eliminating the use of human expert. The updating of parameters through the learning process is facilitated by the gradient vector.

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7 Comparison of Performance Indices between and Controller

A comparison of FLC and ANFIS controller is carried out in terms of different performance indices at various set speeds for different operating conditions. The table 2 shows the comparison for the performance specifications of settling time, steady state error, peak overshoot and time taken to reach the set speed in the reverse direction between FLC and ANFIS controller. The table 2 shows the comparison for the error constants of integral of absolute error (IAE), integral of squared error (ISE), integral of time weighted absolute error (ITAE) and integral of time weighted squared error (ITSE) between FLC and ANFIS controller. The time taken by the BLDC motor to settle at the set speed from standstill is observed for FLC and ANFIS controller at different set speeds of 3500, 3100, 2600, 2100, 1600 and 1100 rpm. It is found that the drive is taking less time with ANFIS controller for all the speeds. ANFIS controller is giving less overshoot at all speeds except at 3100 rpm as compared to FLC. The steady state error is almost equal in both the cases.

8 b) Speed Reversal and Load Perturbation

The drive rotating at set speed is suddenly reversed to rotate in the opposite direction. The ANFIS controller is attaining the set speed in the reverse direction quickly at most of the speeds. When the drive is running under steady state condition, a rated load of 0.662 N-m is applied at time of 0.05 second and withdrawn at 0.12 second. The dip in the speed is slightly high with ANFIS controller as compared to FLC.

9 VI.

10 Conclusion

The simulation model of BLDC motor drive is developed to study the transient and steady state performance with FLC and ANFIS controller. The use of expert in selection of fuzzy control rules and tuning the membership functions is eliminated. The ANFIS controller is giving better performance during the different operating conditions at all speeds as compared to FLC except during the load perturbation where the performance is slightly decreased. [1] [2]

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Figure 1: Fig. 1 :

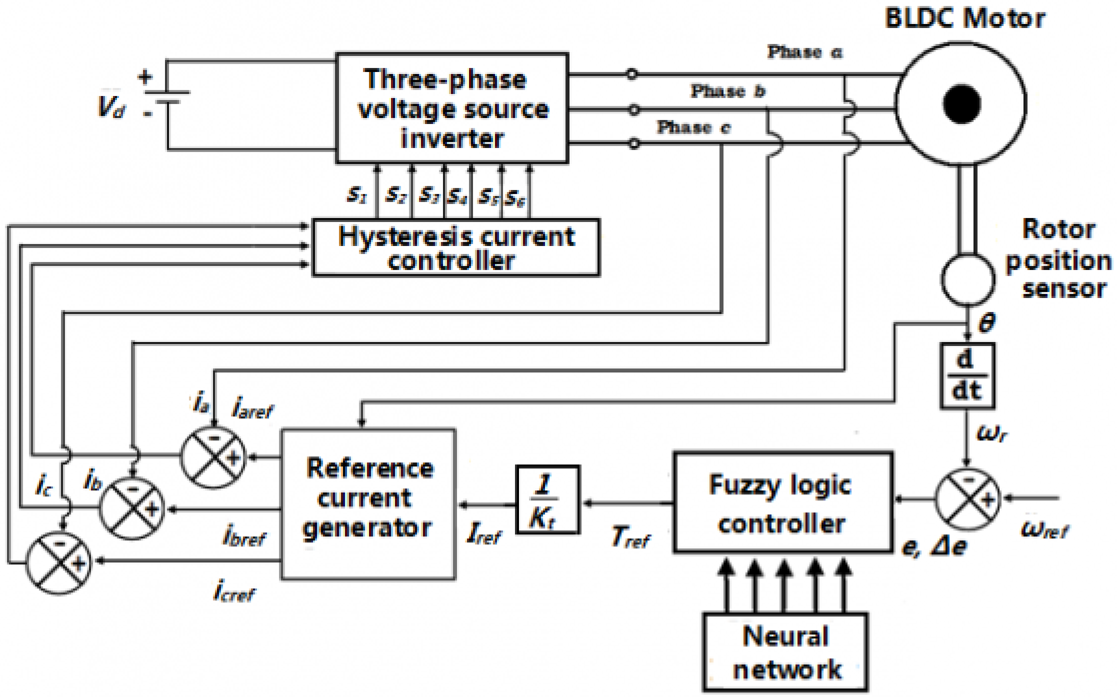

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Figure 2: Figure 2 :

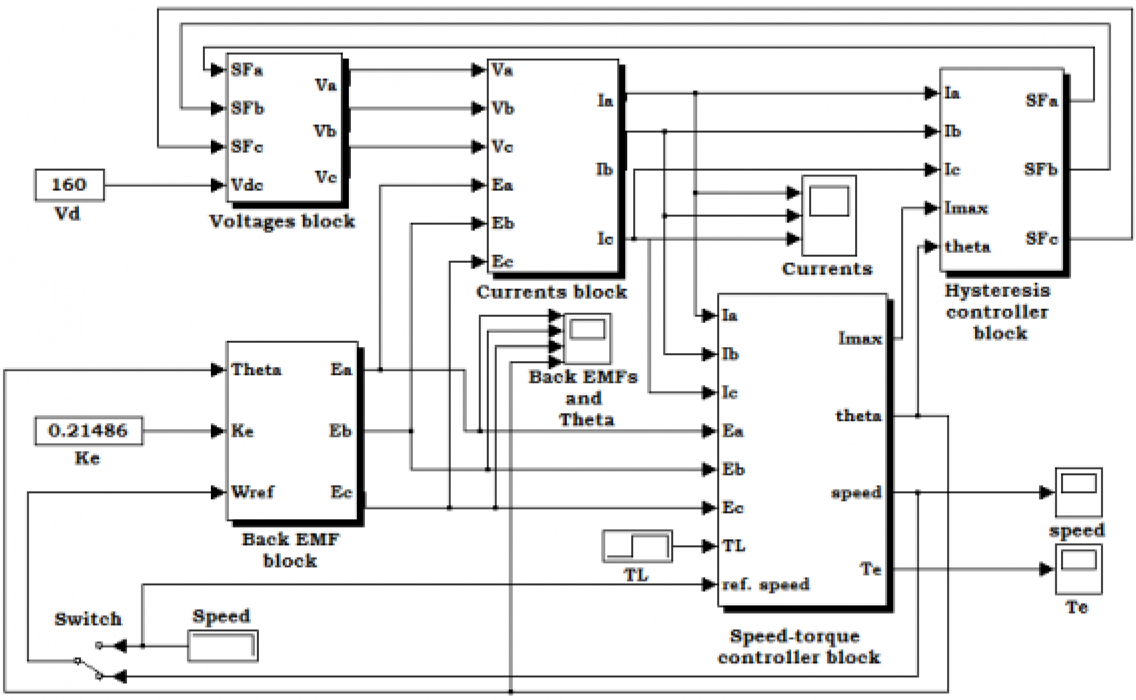

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Figure 3: Figure 3 :

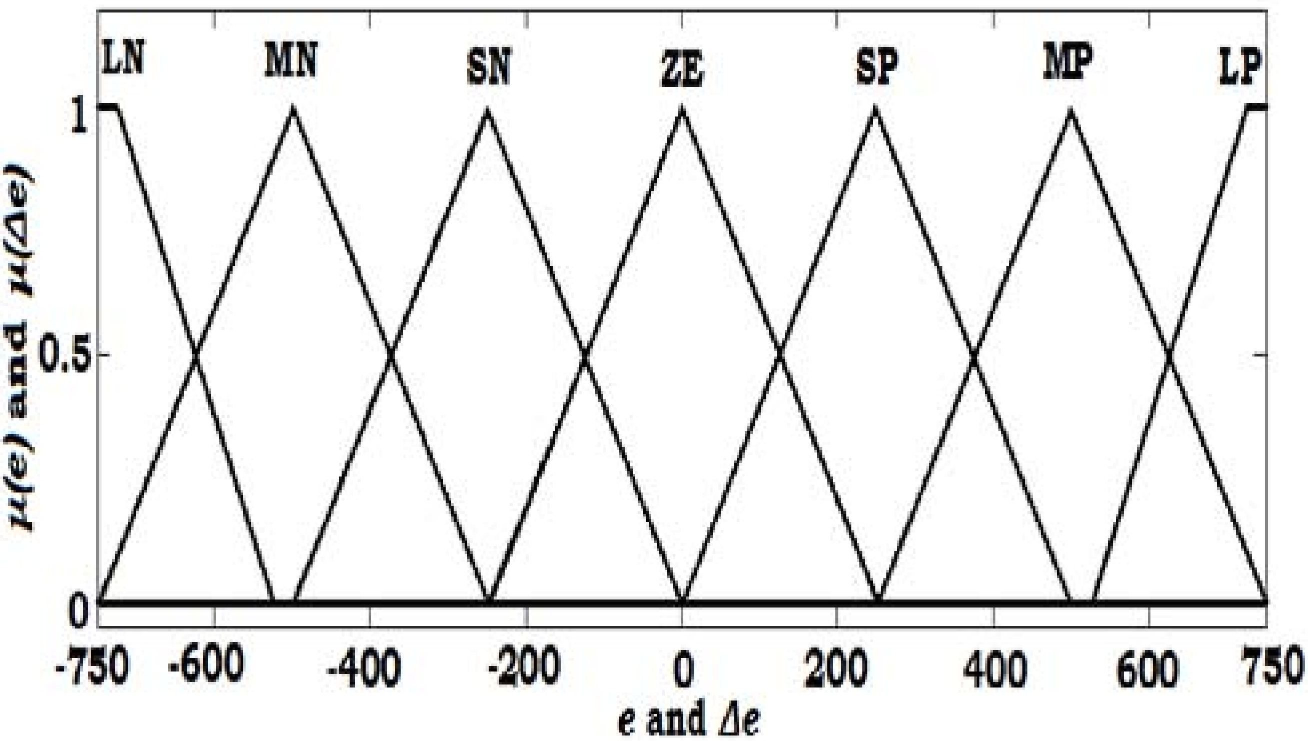

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Figure 4: Figure 4 :

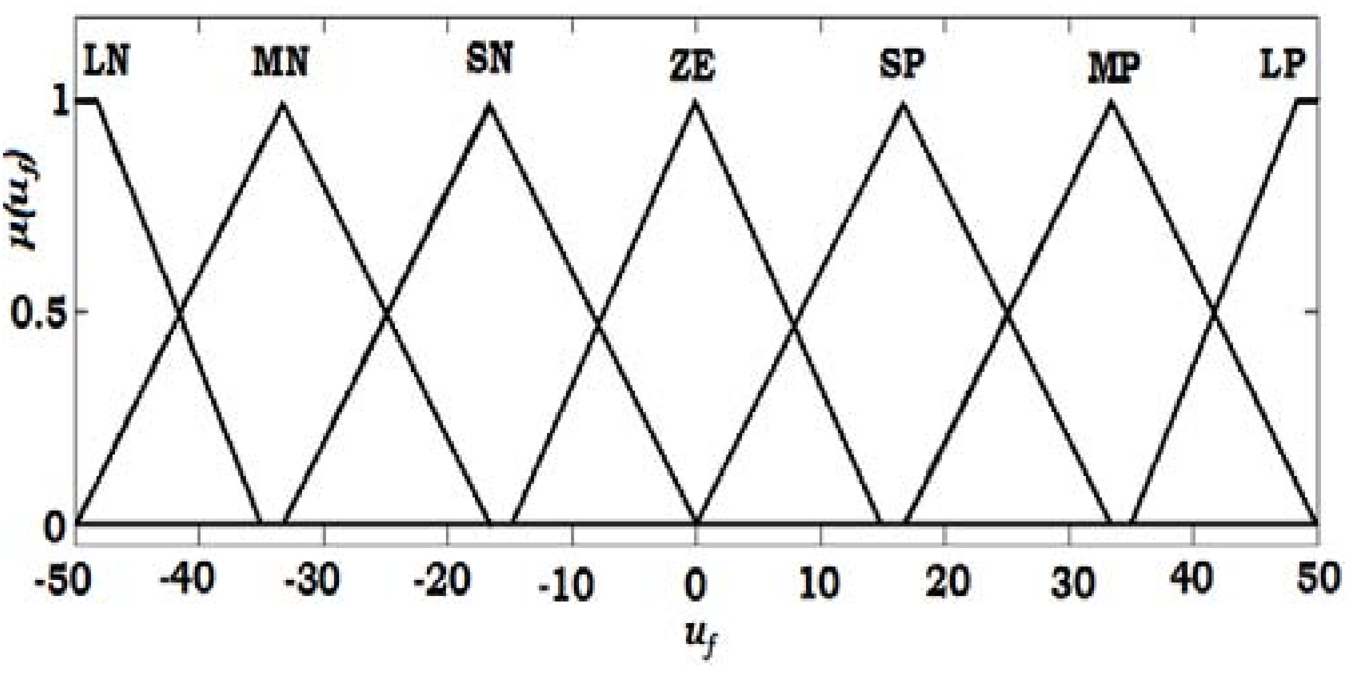

Figure 5:

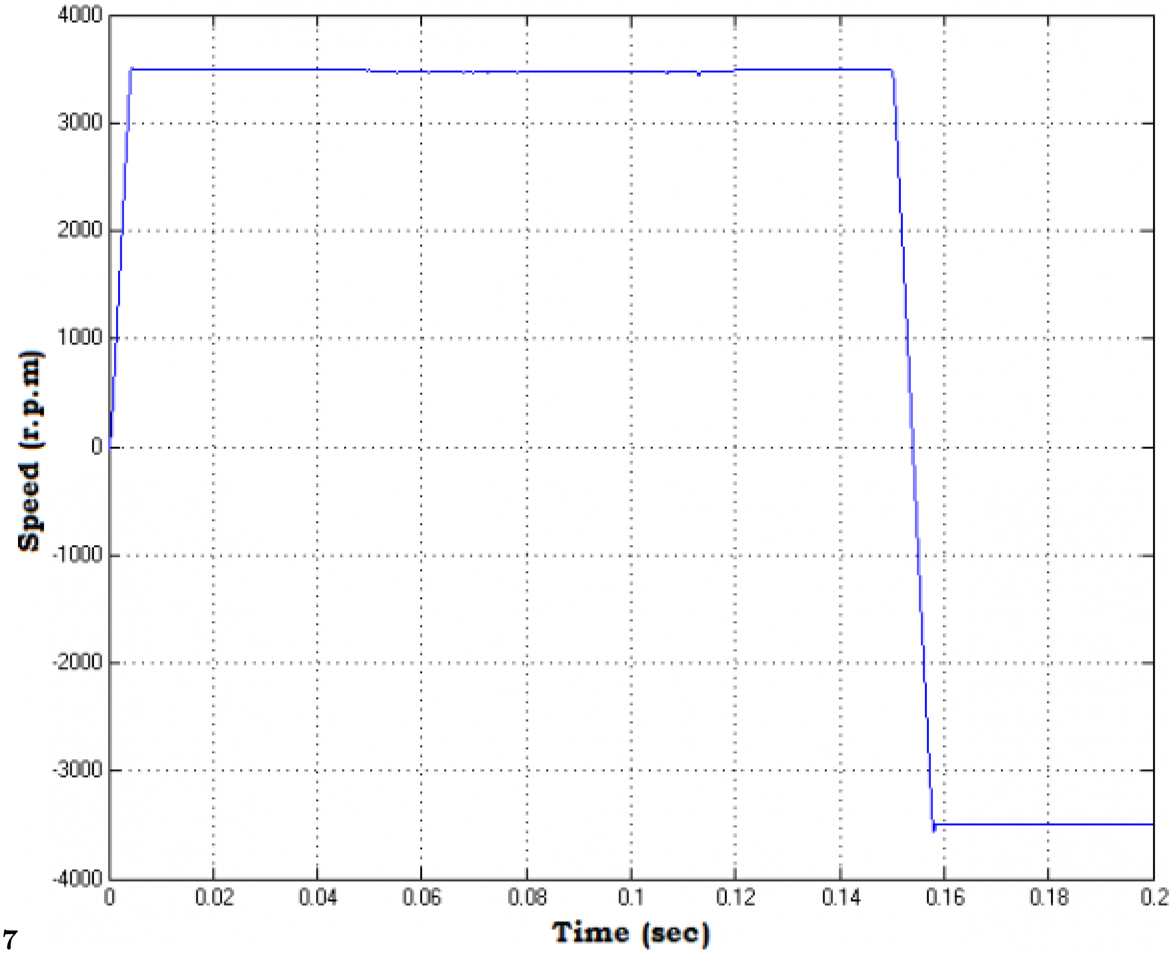

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Figure 6: Figure 5 :Figure 6 :Figure 7 :

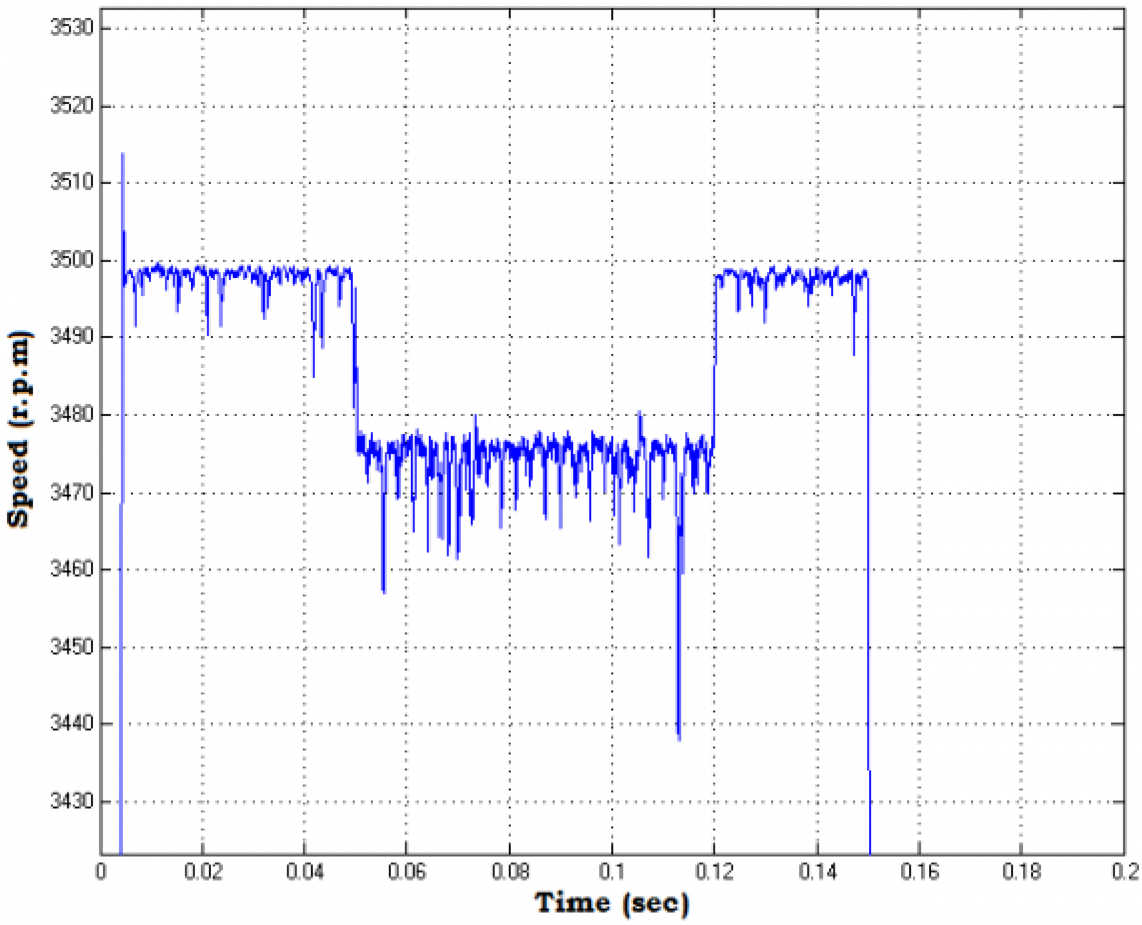

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Figure 7: Figure 8 :Figure 9 :

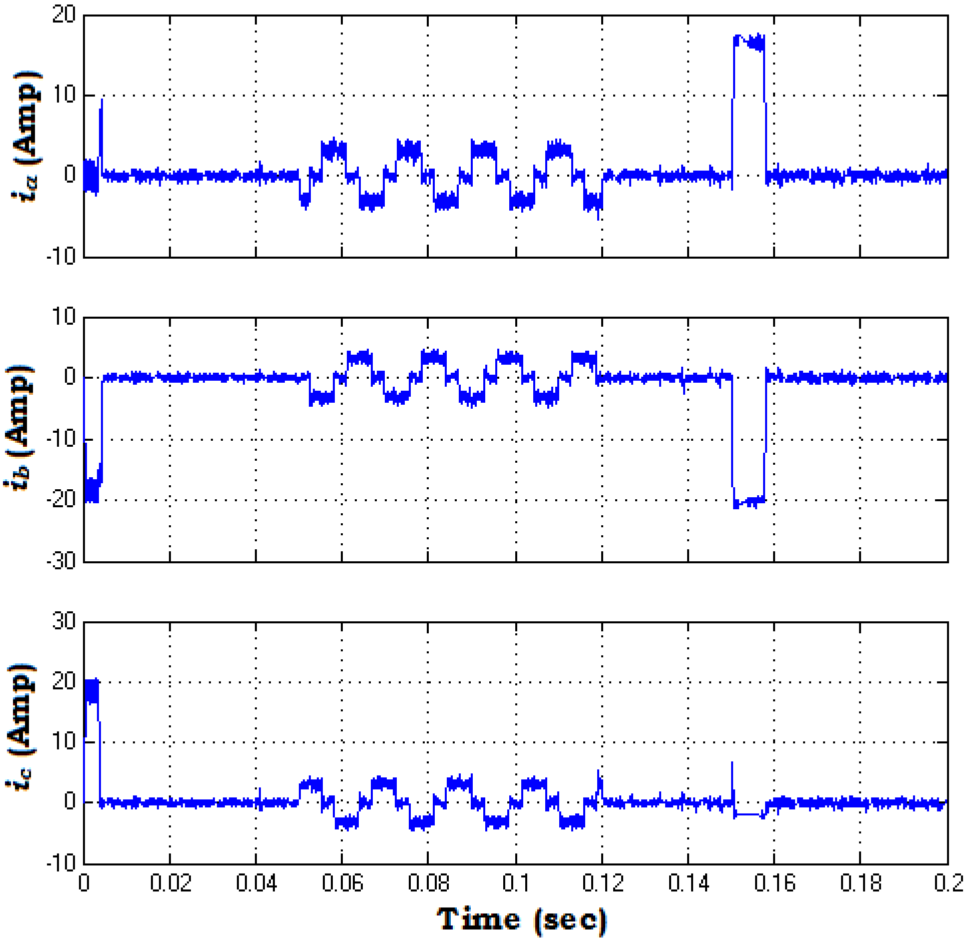

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Figure 8: Figure 11 :

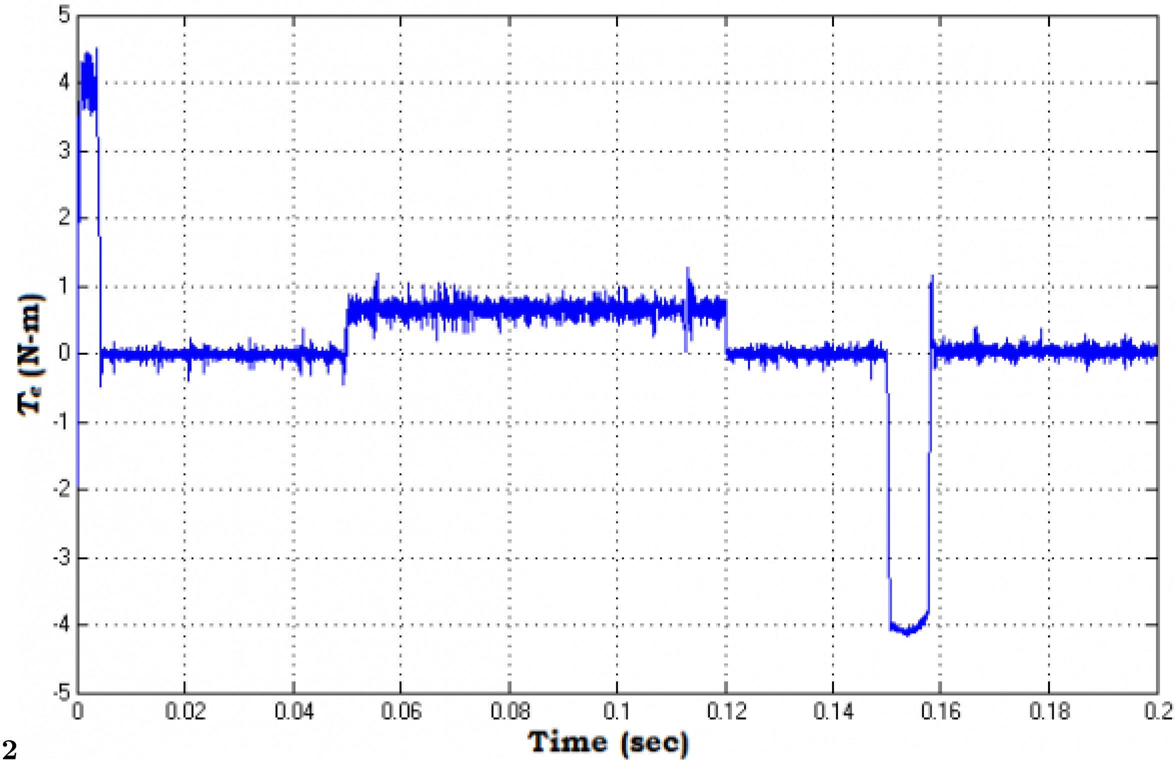

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Figure 9: Figure 12 :

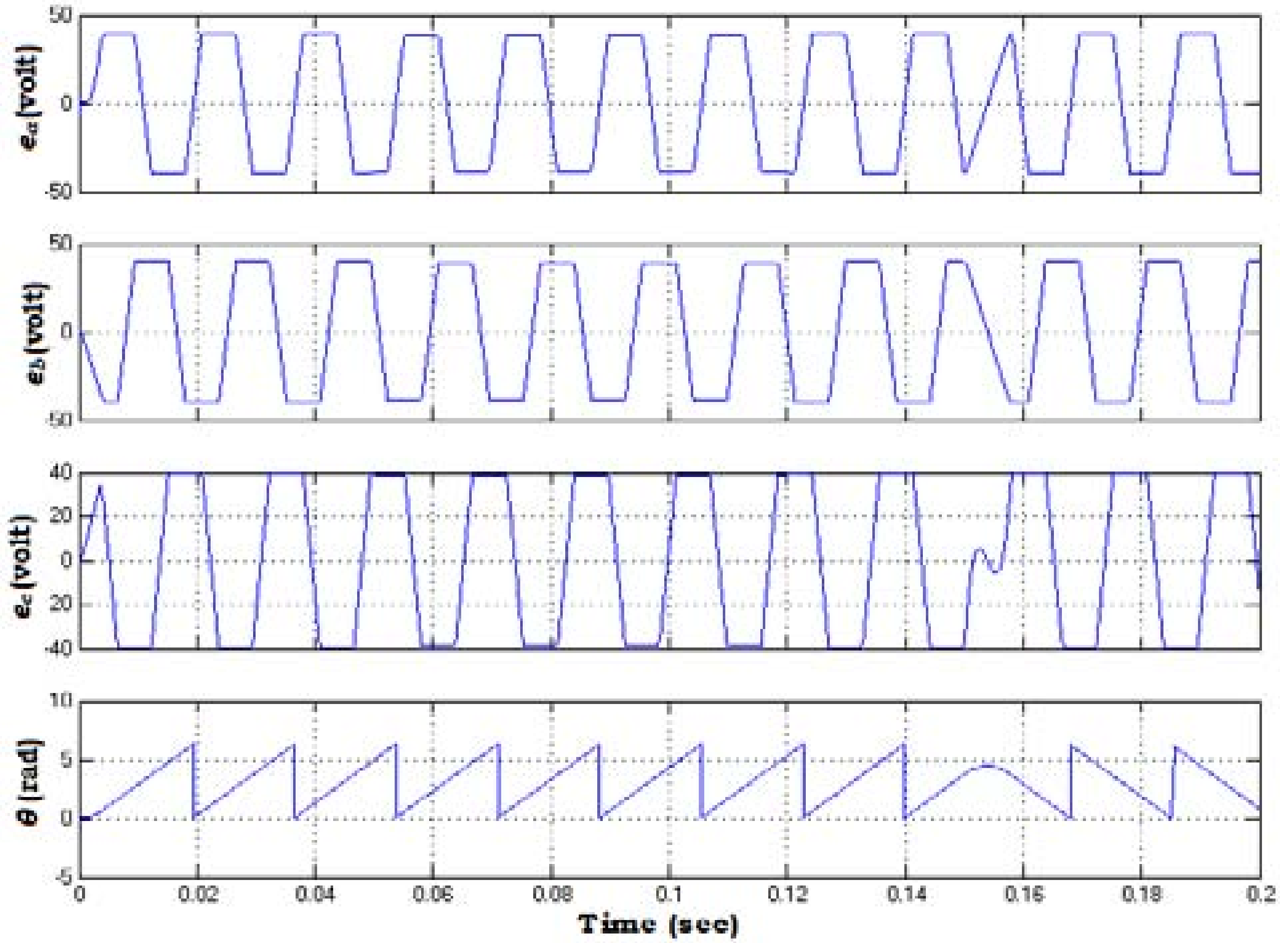

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Figure 10: Figure 10 :Fig. 13 :Fig. 15 :

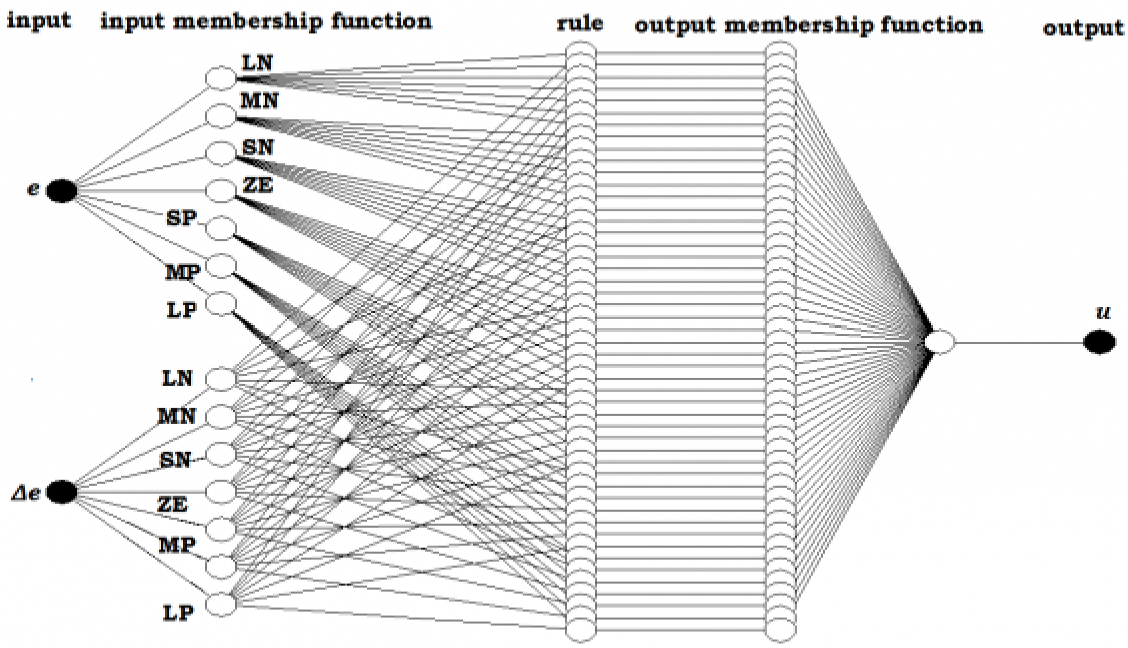

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Figure 11: Fig. 16 :Fig. 18 :

1

LN	LN	LN	LN	MN SN		SN ZE	
MN	LN	MN	MN MN SN			ZE	SP
SN	LN	MN	SN	SN	ZE	SP MP	
ZE	LN	MN	SN	ZE	SP	MP LP	
SP	MN	SN	ZE	SP	SP	MP LP	
MP	SN	ZE	SP	MP MP MP LP			
LP	ZE	SP	SP	MP	LP	LP	LP

Figure 12: Table 1 :

2

Figure 13: Table 2 :

3

Figure 14: Table 3 :

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