

GLOBAL JOURNAL OF RESEARCHES IN ENGINEERING: F ELECTRICAL AND ELECTRONICS ENGINEERING Volume 22 Issue 1 Version 1.0 Year 2022 Type: Double Blind Peer Reviewed International Research Journal Publisher: Global Journals Online ISSN: 2249-4596 & Print ISSN: 0975-5861

Nature Inspired Computing Machine

By Ranganathan Vijayaraghavan & Samanvita Nagaraju

Abstract- An alternate method of representation of number system is proposed. The alternate method is based on reflection (0) and inverted reflection (1). Using an inverted reflected plane, one can create consecutive places of a number system. The alternate method has the advantage of creating empowered system representation with all places of the same power of the base. Three axioms are identified and can be proved by the method of mathematical induction to complete the process. The three axioms are, a creation of number system using inverted reflection, unique non-repetitive inverted reflection count generates natural numbers and taking inverted reflection with sign bit generates two's complement representation of negative numbers. It enabled us to create any desired number system and explained with the help of two versions of the decimal system. Typically, powered system representation leads to random switching within the representation.

Keywords: computing machine; number representation; decimal system; thermometer coding; dual and symmetry; minimal switching; glitch.

GJRE-F Classification: FOR Code: 290901



Strictly as per the compliance and regulations of:



© 2022. Ranganathan Vijayaraghavan & Samanvita Nagaraju. This research/review article is distributed under the terms of the Attribution-NonCommercial-NoDerivatives 4.0 International (CC BY-NC-ND 4.0). You must give appropriate credit to authors and reference this article if parts of the article are reproduced in any manner. Applicable licensing terms are at https://creative-commons.org/ licenses/by-nc-nd/4.0/.

Nature Inspired Computing Machine

Ranganathan Vijayaraghavan ^a & Samanvita Nagaraju ^o

Abstract- An alternate method of representation of number system is proposed. The alternate method is based on reflection (0) and inverted reflection (1). Using an inverted reflected plane, one can create consecutive places of a number system. The alternate method has the advantage of creating empowered system representation with all places of the same power of the base. Three axioms are identified and can be proved by the method of mathematical induction to complete the process. The three axioms are, a creation of number system using inverted reflection, unique non-repetitive inverted reflection count generates natural numbers and taking inverted reflection with sign bit generates two's complement representation of negative numbers. It enabled us to create any desired number system and explained with the help of two versions of the decimal system. Typically, powered system representation leads to random switching within the representation. Contrary to that, new representation provides uniform switching and minimal uniform switching thus minimising glitch. Through this method, one proves that thermometer coding act as a number system so that all arithmetic operations are possible with thermometer coding. New representation brings out the fact that decimal representation confirms to dual form, in binary form is empowered representation and in decimal digit form powered representation. Uniform switching representation of decimal digit resembles the human finger digit, and its dual representation leads to exploiting symmetry.

Keywords: computing machine; number representation; decimal system; thermometer coding; dual and symmetry; minimal switching; glitch.

I. INTRODUCTION

or a long time, the number system (Behroohz Parhami, 2000) is considered to be obtained by the power of the base. This led to the dominance of the binary system as the basis of mechanisation even though humans are more comfortable with the decimal system (GoriiSinaki & Ercegovac, 1981) Moreover, one failed to understand the enormous amount of effort needed to maintain the binary-based system and live with the rigidity dictated by binary system and also а creation of artificial intelligence(Avron Barr, & et al., 1982) thus making humanity to forget the real value human intelligence and make the society machine dependent. After carefully going through the literature by experts like Alan Turing(Turing, 1950; Turing, 1937; Richard P Feynman, 1985; Herbert Simon, 1996; Simon, Herbert

Author α: CEO E-Spectrum Consulting Bangalore.

e-mail: drvrangan@gmail.com

Author o: Associate Professor, Department of Electrical and Electronics Engineering Nitte Meenakshi Institute of Technology, Bangalore. A, 1995) on various aspects of computation led to the thought process that a better representation (WU Ting, et al., 2010) of the number system needed.

The new method of representation of number system through the concept of reflection and inverted reflection explained. Inverted reflection leads to entanglement (Chris Bern Hardt, 2019) and the entire gamut of knowledge of mathematics addresses how to manage the entanglement and device methods to unentangle the system under consideration.

II. New Number System Representation

In nature, two phenomena can happen i.e. reflection and inverted reflection. If reflection (buffer) considered as 0 and inverted reflection (inverters) considered 1, the entire nature is composed of combinations of buffers and inverters. Based on this, define mathematics(Underwood Dudley, 2010) as a subject of study of managing inverted reflection and also considered as entanglement, which is of concern to us. Hence a representation of inverted reflection (not gate) and reflection without inversion (buffer) leads to the creation of number system.

Now a new way to represent the number system is proposed as an inverted reflection. For example, if you have 0 with inverted reflection, 1 gets generated. Thus, with single inverted reflection binary digits are generated. Now consider the two adjacent cells with 0's. Thus, we have 00, 01 and if we take an inverted reflection of this, the series 00 01 | 10 11. Where | denotes inverted reflector. Thus, we have created twobit binary numbers. Now create three bits of two-bit combinations with the most significant bit as 0 to generate 000, 001, 010,011. Now putting an inverted reflection of this series, we generate000,001,010,011| 100,101,110,111.

Thus, three-bit binary numbers are created. In this way, one can create a number system using the inverted reflection, which is much more fundamental than the powered system. It becomes evident that binary representation is the fundamental way to represent any number system to utilise the inverted reflection concept. The second axiom to be satisfied by this concept is that this generated number system creates natural numbers through new inverted reflection but not repeated inversions. Take, for example, two-bit combinations we see that we have natural numbers 0,1 with fresh inverted reflection, 2 with a count two new inverted reflection and 3 with one more new inverted reflection. One more axiom to be needed by inverted reflection to complete the number system generation in creating negative numbers. To create negative numbers if you do inverted reflection with the most significant bit has sign bit then two's complement numbers are generated. In the above explanation if third-bit acts as a sign bit, then the inverted reflection of three bits are two's complement of the two-bit numbers. As seen 000,001,010,011,100,101,110,111 represent 0,1,2,3,0, - 3, -2, -1.

III. Axioms of Representation

To summarise the definition of creation of number system needs to obey three axioms:

- 1. Number systems are generated using inverted reflection of binary equivalent. 2. Natural numbers are generated by counting fresh inverted reflections leading to generation of 1 up to that point.
- 2. Affixing a sign bit and taking inverted reflection with respect that creates negative numbers in two's complement form.

IV. Advantages of New Representation

Let us now analyse what special advantages one gets out of this new way representation of number system.

- It is possible to create arbitrary number systems. For example, thermometer coding (Rasit Onur Topalogulu, 2007; Toru Nakura,& Kunihiro Asada, 2013; Fereshteh Jafarzadehpour et al., 2019) can be proved as a number system.
- 2. One can create empowered number systems in which all binary digits have same power. Thus, first the time, a representation for empowered systems accomplished.
- 3. Particular representations having uniform switching (all switching with the same level of switching), minimal uniform switching (all switching with uniformly one switching only) can be generated that cannot be achieved by powered number system representation.
- Unlike binary representation decimal representation follows dual (Williams, 1996; Girvin, 1996) system of binary empowered and decimal powered system. Thus symmetry (De Haro, Butterfield, 2019; Ruben Aldrovandi et al., 2013; Dr. Ivan Fernandoz-Corbaton, 2019) can be exploited
- A new system satisfies the concept in field of Mathematics(Givant, Steven & HalmosPaul, 2009)as addition, multiplication, subtraction and division is possible and mechanisation realised through Boolean algebra(George Boole, 2011; Foster, 1991) and CMOS technology (Lee, 2019; Rohit Sharma, 2018).

V. Example of New Representation

To illustrate advantages mentioned above decimal representation taken as example.

Consider thermometer coding with nine binary digits of power 2°as shown in Figure 1

		20	20	20	20	20	20	20	20	2 ⁰
00000000	0	o	o	o	o	0	o	o	o	o
00000001	1	o	0	0	0	0	0	0	0	1
00000011	2	o	0	0	0	0	0	o	1	1
000000111	3	o	0	0	0	0	0	1	1	1
000001111	4	o	0	0	о	0	1	1	1	1
000011111	5	o	0	0	0	1	1	1	1	1
000111111	6	o	0	0	1	1	1	1	1	1
001111111	7	o	0	1	1	1	1	1	1	1
011111111	8	0	1	1	1	1	1	1	1	1
111111111	9	1	1	1	1	1	1	1	1	1
			Inverted reflection enabled cell							

Figure 1: A decimal system based on thermometer coding using Inverted Reflection

Thus, the above decimal numbers are generated using inverted reflection satisfying the first axiom, natural numbers are generated using afresh inverted reflection that leads to the generation of 1 and if a sign bit appended and inverted reflection taken produce negative numbers in two's complement form. Thus, with the new concept, a decimal number system is generated. Inherently this representation satisfies uniform and minimal switching (Jaakko Astola & Radomir S. Stankovic, 2006) from one digit to the other. Many advantages of the thermometer coding are already available in the literature and exploited in the hardware (Stanley Wolf, 2002; Sung Kyu Lim, 2008; Holdsworth & Woods, 2003) development as well.

But if one tries to generate addition and multiplication circuits using this representation, it needs huge logic and consumes lot of area. To overcome this and also to illustrate the generation of arbitrary number system consider uniform switching but not minimal. In this representation, we take five bits with four 2¹bits as Most significant bits and the least significant bit with power 2⁰. Then our new decimal numbers are generated using inverted reflection as given in Figure 2.

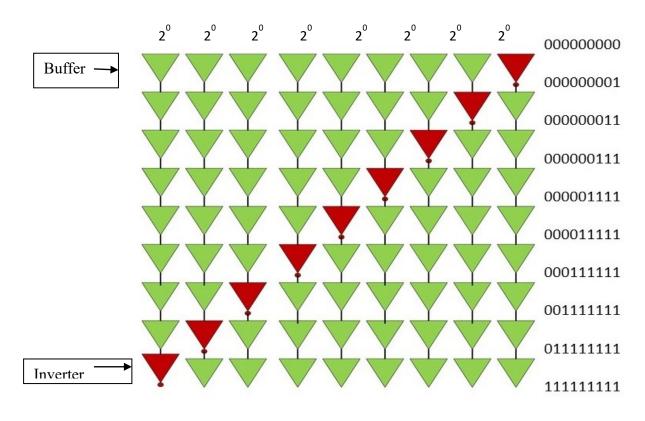
Again, the above decimal system satisfies all three axioms put forward for inverted reflection method of generation of a new number system. Here if you observe the switching is not minimal but uniform. Since only five bits are used, combinations needed to generate arithmetic circuits (LaMeres, 2019; Jiang et al., 2019)will be much less compared to thermometer coding. Here also since there is empowerment for higher-order bits it has the advantages of thermometer coding. Coincidently, human finger systems have such an arrangement and hence form the basis for an analysis of the human digit system.

		21	21	21	21	2 ⁰		
00000000	0	0	0	0	0	0		
00000001	1	0	0	0	0	1		
00000011	2	0	0	0	1	0		
000000111	3	o	0	0	1	1		
000001111	4	o	0	1	1	0		
000011111	5	o	0	1	1	1		
000111111	6	o	1	1	1	0		
001111111	7	o	1	1	1	1		
011111111	8	1	1	1	1	0		
111111111	9	1	1	1	1	1		
		Inverted Reflection Enabled Cell						

Figure 2: Decimal system generation with uniform switching with 5 bits

The number system generation depicted as a machine consisting of inverters and buffers illustrated

in Figure 3 and Figure 4 for the decimal systems discussed earlier.





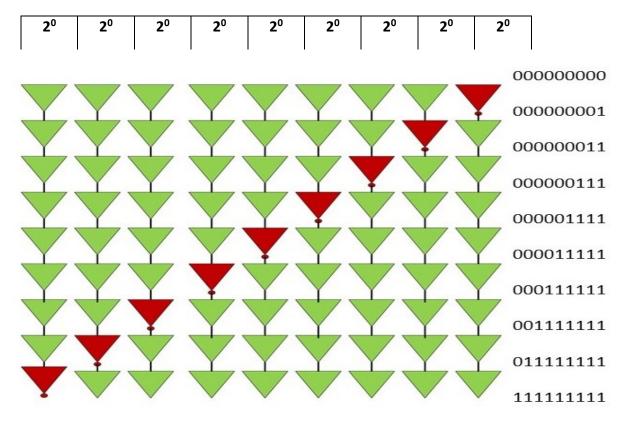


Figure 4: Natural Inverted Reflection Based Machine with uniform switching with weight 2121212120

VI. SCOPE OF NEW REPRESENTATION

This Nature Inspired Machine capable of generation of various number systems. They are considered Natural as it uses various natural aspects like reflection and inverted reflection, new cell generation and existing cell. This brings out the natural generation numbers as a unique count of inverted reflection leading to the generation of new 1's. Any number system consists of a field of buffers and inverters arranged in a matrix of the size based on the weight age given to each element and number of bits taken into consideration.

New decimal representation obeys dual system. At binary level bits are empowered and at digit level digits are powered. This unique advantage gives many applications and mainly manages power and glitch (Ki-Seok Chung et al., 2002) in a better way. This dual nature of empowered and powered enable to create various innovative applications if carefully researched. One immediate observation in human digits applying this dual concept brings out a better understanding of the human system. These pave the way for many indepth studies of the human digits system and bring better understanding. Glitch free circuits become possible through a representation of any number system with equivalent thermometer coding before mechanisation. These become feasible through our new number system representation.

References Références Referencias

- 1. Behroohz Parhami. (2000). Algorithms and Hardware Designs, in Computer Arithmetic Oxford University Press.
- 2. GorjiSinaki, A., & Ercegovac, M. D.(1981). Design of a digit slice on line arithmetic Unit, IEEE 5th Symposium on Computer Arithmetic (ARITH).
- Avron Barr, and Edward, A. FeigenBaumed (1982). Hand Book of Artificial Intelligence, Vol 2, Heuris Tech Press and William Kaufman Inc.
- 4. Turing, A.M. (1950). Computing Machinery and Intelligence, Mind 49:433-460.
- 5. Turing, A.M (1937).On computable Numbers with an application to the Entsschei dungs problem, EDN.
- 6. Richard P Feynman (1985). The computing machines in Furture, Nishina Memorial Lecture at Gakushuin University.
- 7. Herbert A Simon, (1996). The Science of the Artificial, The MIT Press.
- 8. Simon Herbert, A. (1955). On a class of skew Distribution Functions, Biometrika, 42(3-4), 425-440.
- WU Ting, Zhang Min, Huan-qiang, D.U., & WANG Rang-bo. (2010). On optimal binary signed representations of integer, Appl. Math. J, Chinese Univ, 25(3): 331-340.
- 10. Chris Bern Hardt. (2019). Quantum Computing for Everyone, The MIT Press.

- 11. Underwood Dudley. (2010). What is Mathematics for? Notices of the AMS, Vol 57 Number 3.
- 12. Rasit Onur Topalogulu. (2007). Process Variation Aware Multiple-Fault Diagnosis of Thermometer Coded Current-Steering DACs, IEEE Transactions on Circuits and Systems—2: Express Briefs, Vol. 54, No. 2.
- Toru Nakura & Kunihiro Asada. (2013). Pulse Width Controlled PLL/ DLL using Soft Thermometer Code, IEEE Asian Solid-State Circuits Conference A-SSCC.
- Fereshteh Jafarzadehpour, Amir Sabbagh Molahosseini, zadeh Alsadat Emrani Zarandi, & Leonel Sousa, (2019). Efficient Modular Adder Designs Based on Thermometer Coding and One-Hot Coding, IEEE Transactions On Very Large Scale Integration (VLSI) Systems, Vol. 27, NO. 9.
- 15. Williams, H.P. (1996). Duality in Mathematics and linear and integer programming, Journal of Optimization Theory and Applications Vol 90, pages 257–278.
- 16. Girvin, Duality, S M. (1996). in Perspective, Science Vol. 274 issue 5287, pp524-525.
- 17. De Haro, S. (2019). Butterfield, J. On symmetry and duality Synthese.
- Ruben Aldrovandi & José Geraldo Pereira. (2013). Duality Symmetry, In: Teleparallel Gravity. Fundamental Theories of Physics, Vol 173.Springer, Dordrecht.
- 19. Dr. Ivan Fernandoz-Corbaton. (2019). Duality and Symmetry, Special issue of Symmetry, Vol 11, issue , MDPI.
- 20. Givant, Steven, & Halmos Paul, (2009).Introduction to Boolean Algebras, Springer.
- 21. George Boole. (2011). The Mathematical Analysis of Logic, Project Gutenberg's.
- 22. Foster, D.J. (1991).Silicon Processing CMOS Technology, Electronic Materials, in Miller L.S., Mullin J.B. (eds) Electronic Materials. Springer.
- 23. Lee, B.H. (2019). CMOS technology on another level. Nat Electron 2, 272–273.
- 24. Rohit Sharma. (2018). Characterization and Modelling of Digital circuits Second Edition, Paper Back.
- 25. Jaakko Astola, & Radomir S. Stankovic. (2006). Fundamentals of Switching Theory and Logic Design A Hands-on Approach, Springer US.
- 26. Stanley Wolf. (2002). Silicon Processing for VLSI Era, Vol 4 Deep-Submicron Process Technology, Lattice Pr.
- Sung Kyu Lim. (2008). Practical Problems in VLSI Physical Design Automation, Springer. Holdsworth, B., & Woods, R.C. (2003). Arithmetic Circuits, in Digital Logic Design Elsevier Ltd.
- 28. LaMeres, B.J. (2019). Arithmetic Circuits. In: Introduction to Logic Circuits & Logic Design with Verilog. Springer, Cham.

- 29. Jiang, H., Liu L., Lombardi, F., & Han, J. (2019) Approximate Arithmetic Circuits: Design and Evaluation. In: Reda S., Shafique M. (eds) Approximate Circuits. Springer, Cham.
- Ki-Seok Chung, Taewhan Kim & Liu, C. L. (2002). A Complete Model for Glitch Analysis in Logic Circuits, Journal of Circuits, Systems and Computers Vol. 11, No. 02, pp. 137-153.