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Improvement of Power System Stability by using UPFC with Cascade Proportional Integral Differential Controller Pranoy Kumar Singha Roy¹ and MD Nasmus Sakib Khan Shabbir² IRUET, BANGLADESH Received: 7 December 2013 Accepted: 31 December 2013 Published: 15 January 2014

7 Abstract

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In power system, one most crucial problem is maintaining system stability. The main reasons 8 for occurring stability problem in the system is due to the fault occurs in the system. This 9 paper presents the model of a Unified Power Flow Controller (UPFC) which is externally 10 controlled by a cascade Proportional Integral Differential (PID) controller for the 11 improvements of voltage stability on line power system. The cascade PID controller 12 parameters has been selected by using Tyreus-Luyben settings method for primary loop 13 controller and modified Ziegler-Nichols method for secondary loop controller. Cascade control 14 is mainly used to achieve fast rejection of disturbance before it propagates to the other parts 15 of the plant.PID controller in cascade architecture is the best choice compared to conventional 16 single loop control system for controlling nonlinear processs. The primary controller is used to 17 calculate the setpoint for the secondrary controller. Both single phase and three phase faults 18 have been considered in the research. In this paper, A power system network is considered 19 which is simulated in the phasor simulation method the network is simulated in three steps; 20 without UPFC, With UPFC but no externally controlled, UPFC with cascade PID. 21 Simulation result shows that without UPFC, the system parameters becomes unstable during 22 faults. When UPFC is imposed in the network, then system parameters becomes stable. 23 Again, when UPFC is controlled externally by cascade PID controllers, then system 24 parameters (V,P,Q) becomes stable in faster way then without controller. It has been 25 observed that the UPFC ratings are only 10 MVA with controllers and 100 MVA without 26 controllers. So, UPFC with cascade PID controllers are more effective to enhance the voltage 27 stability and increases power transmission capacity of a power system. The power system 28 oscillations is also reduced with controllers in compared to that of without controllers. So with 29 cascade PID controllers the system 30

Index terms — UPFC, voltage regulator, cascade propotional integral differential controller, matlab simulink. 32 Abstract-In power system, one most crucial problem is maintaining system stability. The main reasons for 33 34 occurring stability problem in the system is due to the fault occurs in the system. This paper presents the model 35 of a Unified Power Flow Controller (UPFC) which is externally controlled by a cascade Proportional Integral Differential (PID) controller for the improvements of voltage stability on line power system. The cascade PID 36 controller parameters has been selected by using Tyreus-Luyben settings method for primary loop controller and 37 modified Ziegler-Nichols method for secondary loop controller. Cascade control is mainly used to achieve fast 38 rejection of disturbance before it propagates to the other parts of the plant.PID controller in cascade architecture 39 is the best choice compared to conventional single loop control system for controlling nonlinear processs. The 40 primary controller is used to calculate the setpoint for the secondrary controller. Both single phase and three 41

42 phase faults have been considered in the research. In this paper, A power system network is considered which is 43 simulated in the phasor simulation method & the network is simulated in three steps; without UPFC, With UPFC 44 but no externally controlled, UPFC with cascade PID. Simulation result shows that without UPFC, the system 45 parameters becomes unstable during faults. When UPFC is imposed in the network, then system parameters

becomes stable. Again, when UPFC is controlled externally by cascade PID controllers, then system parameters

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48 only 10 MVA with controllers and 100 MVA without controllers. So, UPFC with cascade PID controllers are

⁴⁹ more effective to enhance the voltage stability and increases power transmission capacity of a power system. The

50 power system oscillations is also reduced with controllers in compared to that of without controllers. So with

51 cascade PID controllers the system performance is greatly enhanced.

52 1 Introduction

ACTS can convenience the power flow control, increases the power transfer capability, enhance the security and stability, decrease the generation cost of the power system [1]- [2]. UPFC is one kind of Authors??? is Dept. of EEE, Rajshahi University of Engineering & Technology, Rajshahi, Bangladesh. e-mails: pronoy331@yahoo.com, ENG.SAKIB@gmail.com FACTs device which can be installed in series in the transmission lines [3]. It is used to control the power flow along the transmission line and thus to meet the needs of power transfer. UPFC consists of a series and shunt converter that is connected by a common DC link capacitor. UPFC performs simultaneously the function of transmission line real and reactive power flow control in addition to UPFC bus voltage shunt reactive power control. The parameters (voltage, impedence, and phase angle) affecting power flow in the transmission line which can be controlled by the shunt converter of the UPFC. The series converter of the UPFC injects a series voltage of adjustable magnitude and phase angle in the transmission line and controls real and reactive power flow in the transmission line [7]-??9]. The dynamic nature of the UPFC lies in the use of thyristor devices (e.g. GTO, IGCT).Therefore, this paper presents thyristor based UPFC controllers to improve

66 the performance of multimachine power system.

67 **2** II.

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68 3 Control Concept of upfc

UPFC is a FACTS device used for improving power quality in power systems is shown in fig1. The UPFC consists 69 70 of combination of series converter and shunt converter. The DC terminals of shunt device are connected to a 71 common link DC capacitor. The shunt converter of the UPFC controls the UPFC bus voltage/shunt reactive 72 power and the dc link capacitor voltage. In this case, the shunt converter voltage is decomposed into two components. One component is in phase and the other in quqdrature with the UPFC bus voltage. Decoupled 73 control system has been employed to achive simultaneous control of the UPFC bus voltage and the DC link 74 capacitor voltage. The series converter of the UPFC provides simultaneous control of real and reactive power 75 flow in the transmission line. The series converter injected voltage is decomposed into two component. One 76 component of the series injected Year 2014 voltage is in quadrature and the other in phase with the controls the 77 transmission line real power flow. This strategy is similar to that of a phase shifter. The in phase component 78 controls the transmission line reactive power flow. This strategy is similar to that of a tap changer. 79

⁸⁰ 4 UPFC bus voltage. The quadrature injected component

⁸¹ 5 Power System Model

This example described in this section illustrates modeling of a simple transmission system containing 2-hydraulic 82 power plants in Fig. ??. The power grid consists of two power generation substations [10]. Complete simulink 83 model is shown in Fig. ??. A UPFC is used to control the power flow in a 500 kV /230 kV transmission system. 84 The system, connected in a loop configuration, consists essentially of five buses (B1 to B5) interconnected through 85 three transmission lines (L1, L2, L3) and two 500 kV/230 kV transformer banks Tr1 and Tr2. Two power plants 86 located on the 230 kV system generate a total of 1500 MW which is transmitted to a 500 kV, 15000 MVA equivalent 87 and to a 200 MW load connected at bus B3. Each plant model includes a speed regulator, an excitation system 88 as well as a power system stabilizer (PSS). In normal operation, most of the 1200 MW generation capacity of 89 power plant #2 is exported to the 500 kV equivalent through two 400 MVA transformers connected between 90 buses B4 and B5. Complete Simulink model has shown in Fig. ??. 91

92 **6** IV.

93 7 Simulation Results

⁹⁴ The load flow solution of the above system is calculated and the simulation results are shown below. Two types ⁹⁵ of faults: A. single line to ground fault &B. Three phase fault have been considered.

⁹⁶ 8 Designe of Cascade Propotional Integral Differtional Con ⁹⁷ troller (pid)

The Tyreus-Luyben [10] procedure is quite similar to the Ziegler-Nichols method but the final controller settings are different. Tyreus-Luyben PID Controller, the values of delay time, rise time, and settling time are better in comparison with Modified Ziegler-Nichols method. Also this method only proposes settings for PI and PID controllers. These settings that are based on ultimate gain and period are given in table 1. For some control loops the measure of oscillation, provide by ¼ decay ratio and the corresponding large overshoots for set point changes are undesirable therefore more conservative methods are often preferable such as modified Z-N settings. ? + + = Gc(s)=Kp(1+?? S i T + S d T)

Figure 12 : PID controller is in proportional action For selecting the proper controller parameters, Tyreus-Luyben Tuning Method is described below.

In this method, the parameter is selected as T i =?, T d =0. Using the proportional controller action [Fig. 12] only increase K p from 0 to a critical value K cr. At which the output first exhibits sustained oscillations [Fig. (F_{ij}, F_{ij})] increase K p from 0 to a critical value K cr. At which the output first exhibits sustained oscillations [Fig. (F_{ij}, F_{ij})] increase K p from 0 to a critical value K cr. At which the output first exhibits sustained oscillations [Fig. (F_{ij}, F_{ij})] increase K p from 0 to a critical value K cr. At which the output first exhibits sustained oscillations [Fig. (F_{ij}, F_{ij})] in the formula of the formul

109 13]. Thus the critical gain K cr & the corresponding period P cr are experimentally determined. It is suggested

that the values of the parameters K p T i T d should set according to the following formula same as Zieglar-Nicles methods. In this method, the parameter is selected as T i =?,T d =0.

112 9 Simulation Results

113 The network remains same [Fig. ??

114 10 R esults & D iscussions

115 The performance of the proposed PID Controller with UPFC has been summarized in the table-II. In table-II,

116 ? (infinite time) means the system is unstable, UPFC rating in MVA. The network is simulated in three steps;

without UPFC, With UPFC, UPFC with proposed PID Controller.
T able 2 : Performance of Proposed PID Controller VIII.

119 11 C onclusion

120 This paper presents the power system stability improvement i.e. voltage level, machine oscillation damping, real

¹²¹ & reactive power in a power system model of UPFC without or with proposed cascade PID Controller for different ¹²² types of faulted conditions. Cascade PID is also a very efficient controller then From above results, this proposed

Types of faithed conditions. Cascade 11D is also a very encient controller then From above results, this proposed Types-Luyben setting method for selecting for primary PID controller parameters and modified Ziegler-Nichols

124 method for Secondary PID controller. In cascade PID Controller may be highly suitable as a UPFC controller

¹²⁵ because of shorter stability time, simple designed, low cost & highly efficient controller. Rather that, If cascade

PID controller is used then only small rating of UPFC becomes enough for stabilization of robust power system

within very conditions. These proposed cascade PID Controller can be applied for any interconnected multimachine power system network for stability improvement. These controller can be applied to another FACTS

devices namely SSSC, STATCOM, SVC whose controllers may be controlled externally by designing different

types of controllers which also may be tuned by using different algorithm i.e. Fazzy logic, ANN, Genetic algorithm,

131 FSO etc. for both transient and steady state stability improvement of a power system.

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Figure 1: Figure 1 :

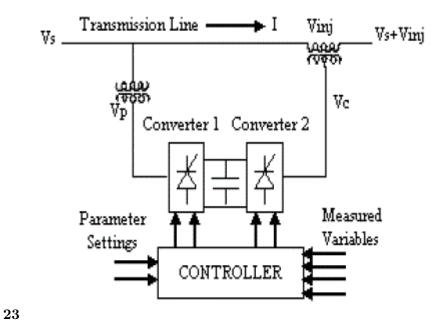


Figure 2: Figure 2 : Figure 3 :

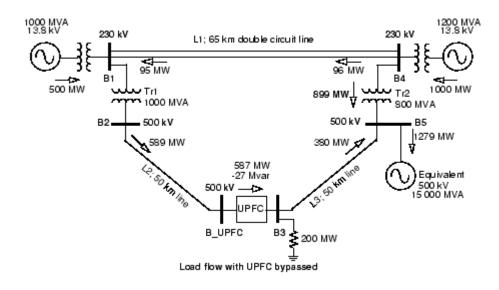


Figure 3:

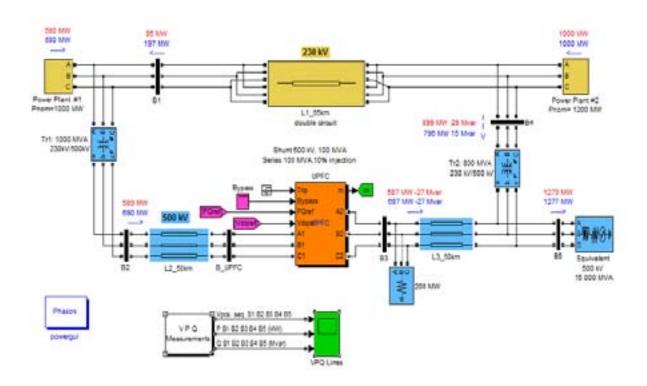
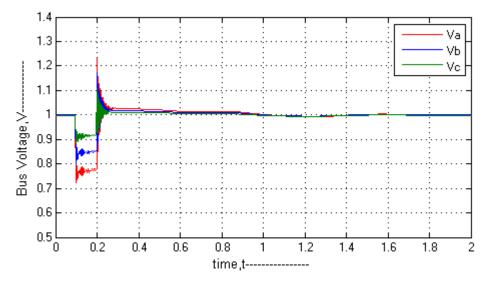


Figure 4:



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Figure 5: Figure 4 : Figure 5 : Figure 6 : Figure 7 :

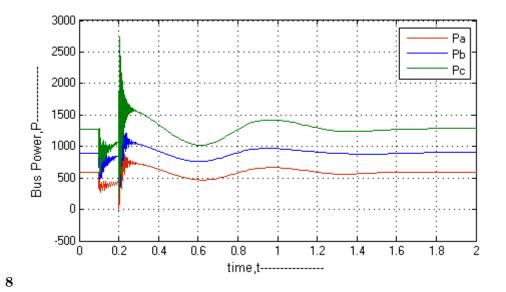


Figure 6: Figure 8 :

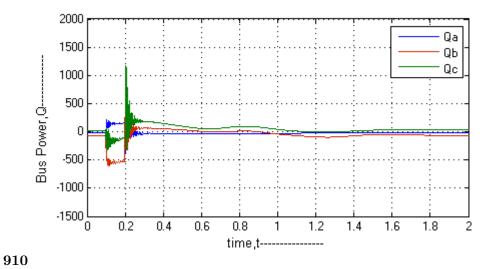


Figure 7: Figure 9 : Figure 10 :

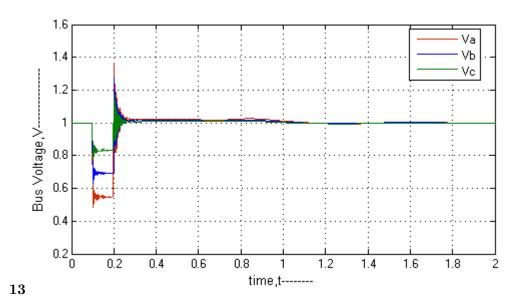


Figure 8: Figure 13 :

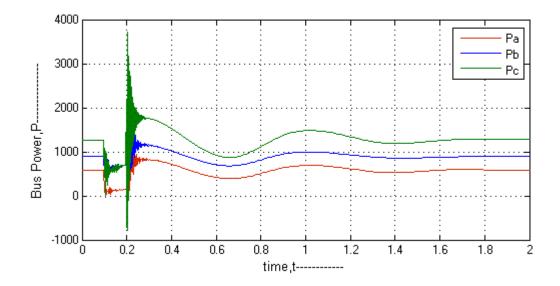


Figure 9:

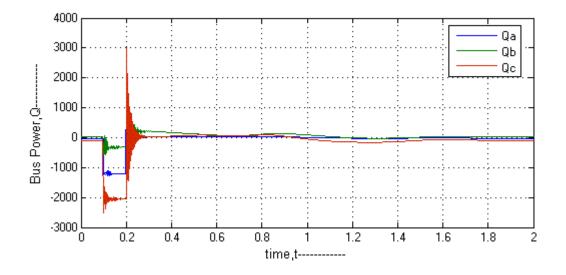


Figure 10:

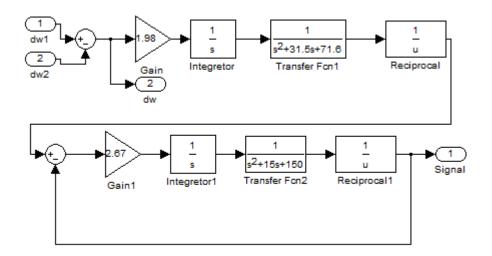


Figure 11:

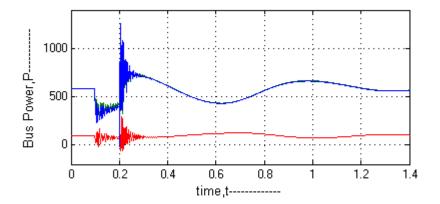
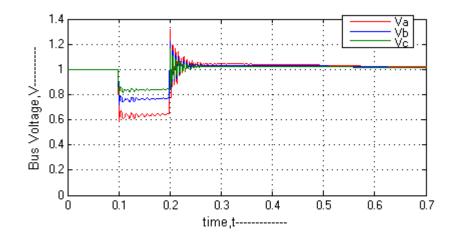


Figure 12:



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Figure 13: Figure 16 :

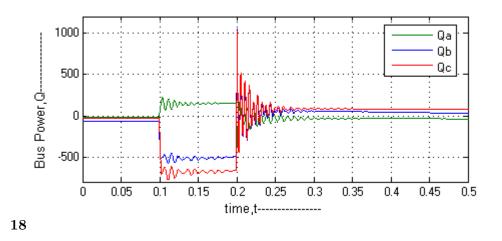
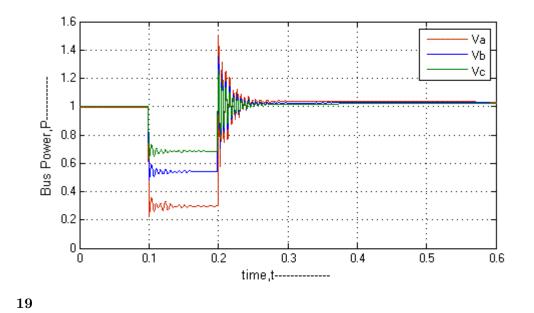


Figure 14: Figure 18 :



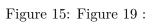


Figure 16:

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Controller	Кр	Ti	Td
PI	Kcr/3.2	2.2Pcr	
PID	Kcr/3.2	2.2Pcr	Pcr/6.3

Figure 17: Table 1 :

$\mathbf{2}$

Controller PI PID			Kp 0.2Kcr 0.2Kcr			Pcr/2 Pcr/2		Ti			
a) Designed of PID Controller											
	PID controller i	is tuned	by the p	ropose	$^{\mathrm{ed}}$						
Tyreus-Luyben tuning methods. The PID of	controller has										
three term control signal											
u	t () Кр	e(t)	i	e	t ()	dt	Κ	р	Т
				Т							d
				Κ							
				р							

Figure 18: Table 2 :

11 C ONCLUSION

132 .1 Status

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